

# A Knowledge Compilation Map for Ordered Real-Valued Decision Diagrams

Full Version — rev. 3

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## Abstract

Valued decision diagrams (VDDs) are data structures that represent functions mapping variable-value assignments to non-negative real numbers. They prove useful to compile cost functions, utility functions, or probability distributions. While the complexity of some queries (notably optimization) and transformations (notably conditioning) on VDD languages has been known for some time, there remain many significant queries and transformations, such as the various kinds of cuts, marginalizations, and combinations, the complexity of which has not been identified so far. This paper contributes to filling this gap and completing previous results about the time and space efficiency of VDD languages, thus leading to a knowledge compilation map for real-valued functions. Our results show that many tasks that are hard on valued CSPs are actually tractable on VDDs.

## 1 Introduction

Valued decision diagrams (VDDs) are data structures that represent multivariate functions  $f$  having a set  $\mathcal{V}$  of valuations (often a subset of  $\mathbb{R}^+$ ) as codomain; such functions are typically cost functions, utility functions, or probability distributions, and as such, are considered in a number of AI applications. Among the various tasks of interest when dealing with such functions are *optimization* queries: find an assignment of the variables leading to an optimal valuation; find a value of a given variable that can be extended to an optimal assignment; etc. Optimization queries are particularly valuable when combined with the *conditioning* transformation, which derives a representation of a restriction of  $f$  obtained by assigning some of its variables. For instance, the following task can be handled by combining optimization with conditioning: in configuration problems, when  $f$  represents a cost function mapping each assignment (say, a car) to its price, output the cheapest car with seven seats; or, when  $f$  represents a probability distribution linking diseases to symptoms, return the most probable explanation (the most probable disease given a set of symptoms).

Many other data structures have been defined for representing such multivariate functions  $f$ , valued CSPs (Schiex, Fargier, and Verfaillie 1995), GAI nets (Bacchus and Grove

1995), and Bayes nets (Pearl 1989) being among the best-known. However, they are not adapted to the aforementioned requests when guaranteed response times are required (as is the case in Web-based applications): optimization is indeed NP-hard on CSPs, GAI nets, and Bayes nets.

Contrastingly, optimization is a tractable query on VDDs; and conditioning is tractable as well. This explains why several families of VDDs have been defined and studied in the past twenty years. These include the language ADD of algebraic decision diagrams (Bahar et al. 1993), the language AADD of affine algebraic decision diagrams (Tafertshofer and Pedram 1997; Sanner and McAllester 2005), and the language SLDD of semiring-labeled decision diagrams (Wilson 2005). Actually, SLDD is itself a family of languages  $\text{SLDD}_{\otimes}$ , parameterized by a binary operator  $\otimes$ , yielding in particular  $\text{SLDD}_+$ , or equivalently EVBDD (Lai and Sastry 1992; Lai, Pedram, and Vrudhula 1996; Amilhastre, Fargier, and Marquis 2002), when  $\otimes$  is  $+$ , and  $\text{SLDD}_{\times}$  when  $\otimes$  is  $\times$ .

There nevertheless exist many queries and transformations of interest that do not amount to a combination of conditioning and optimization. Consider for instance the following request: “tell me whether among the ‘cheap’ cars (say, with a price lower than 10,000 euros) of this given type, there is one that has seven seats”. It requires one to focus on the set of ‘cheap’ cars, which is neither optimization nor conditioning. Similarly, some transformations, such as projection on variables of interest, or its dual, variable elimination (e.g., forgetting or marginalization), also being of tremendous value (e.g., to solve the “posterior marginal” problem (PM) in Bayes nets), are not reducible to optimization and conditioning. This is also the case of combination transformations (a.k.a. ‘apply’ operations), which given the representations of two functions  $f$  and  $g$ , compute a representation of  $f \odot g$ ,  $\odot$  being an associative and commutative operator on  $\mathcal{V}$  (e.g., addition or product when  $\mathcal{V} = \mathbb{R}^+$ ). Such transformations are very important, if only for incrementally generating representations in a bottom-up way.

The knowledge compilation (KC) map (Darwiche and Marquis 2002) identifies the complexity of requests (i.e., queries and transformations) over many propositional languages, as well as their relative succinctness. However, it has been drawn for the specific case of Boolean functions; although it has been extended in several directions, as of yet no such map exists for the VDD languages. It has been

shown that AADD is strictly more succinct than ADD (Santer and McAllester 2005), and the succinctness picture has later been completed (Fargier, Marquis, and Schmidt 2013): AADD is strictly more succinct than both SLDD<sub>+</sub> and SLDD<sub>×</sub>, and both SLDD<sub>+</sub> and SLDD<sub>×</sub> are in turn strictly more succinct than ADD. To complete the map, one now needs to provide the set of requests that each language satisfies; in order words, for each request, and for each language among ADD, SLDD<sub>+</sub>, SLDD<sub>×</sub>, and AADD, to either find a polynomial-time algorithm computing the request, or prove that no such algorithm exists unless P = NP.

This is the main goal of this paper, which is organized as follows. The next section presents the family of VDD languages, and the following one formally defines the queries and transformations used. Then complexity results are presented,<sup>1</sup> that establish whether each VDD language satisfies each query or transformation. This paves the way for a KC map for functions that are not essentially Boolean.

## 2 Valued Decision Diagrams

**Preliminaries.** Given a finite set  $\mathcal{X} = \{x_1, \dots, x_n\}$  of variables, each  $x_i$  ranging over a finite domain  $D_{x_i}$ , and any set  $X \subseteq \mathcal{X}$ ,  $\vec{x} = \{(x_i, d_i) \mid x_i \in X, d_i \in D_{x_i}\}$  denotes an assignment of the variables from  $X$ ;  $D_X$  is the set of all such assignments (the Cartesian product of the domains of the variables in  $X$ ). The concatenation of two assignments  $\vec{x}$  and  $\vec{y}$  of two disjoint subsets  $X$  and  $Y$  is an assignment of  $X \cup Y$  denoted  $\vec{x} \cdot \vec{y}$ .

We consider functions  $f$  of variables from a subset  $\text{Scope}(f) \subseteq \mathcal{X}$  to some set  $\mathcal{V}$  (this paper often assumes  $\mathcal{V} = \mathbb{R}^+$ ). The domain of  $f$  is denoted as  $D_f = D_{\text{Scope}(f)}$ . For any  $Z \subseteq \text{Scope}(f)$ ,  $f_{\vec{z}}$  denotes the *restriction* (or *semantic conditioning*) of  $f$  by  $\vec{z}$ , that is, the function on  $\text{Scope}(f) \setminus Z$  such that for any  $\vec{x} \in D_{\text{Scope}(f) \setminus Z}$ ,  $f_{\vec{z}}(\vec{x}) = f(\vec{z} \cdot \vec{x})$ .

Given a binary operator  $\odot$  over  $\mathcal{V}$  and two functions  $f$  and  $g$  sharing the same scope,  $f \odot g$  is the function defined by  $f \odot g(\vec{x}) = f(\vec{x}) \odot g(\vec{x})$ . The  $\odot$ -projection of  $f$  on  $Z \subseteq \text{Scope}(f)$  is defined by  $f^{\odot, Z}(\vec{x}) = \odot_{\vec{y} \in D_{\text{Scope}(f) \setminus Z}} f_{\vec{y}}(\vec{x})$ .

Slightly abusing notations, if  $X$  and  $Y$  are two disjoint sets of variables,  $\text{Scope}(f) = X$ , and  $\vec{x} \cdot \vec{y}$  is an assignment of  $X \cup Y$ , then we assume that  $f(\vec{x} \cdot \vec{y}) = f(\vec{x})$ ; in practice, we often consider complete assignments, i.e.,  $\vec{x} \in D_{\mathcal{X}}$ .

Given  $\succeq$  a reflexive and transitive relation on  $\mathcal{V}$  (i.e., a preorder), of which we denote  $\sim$  the symmetric part and  $\succ$  the asymmetric part, we can define for any  $\gamma \in \mathcal{V}$  the following sets, referred to as “cuts”:

- $CUT^{\max}(f) = \{\vec{x}^* \mid \forall \vec{x}, \neg(f(\vec{x}) \succ f(\vec{x}^*))\}$ ;
- $CUT^{\min}(f) = \{\vec{x}^* \mid \forall \vec{x}, \neg(f(\vec{x}) \prec f(\vec{x}^*))\}$ ;
- $CUT^{\succeq \gamma}(f) = \{\vec{x} \mid f(\vec{x}) \succeq \gamma\}$ , and using similar definitions,  $CUT^{\preceq \gamma}(f)$  and  $CUT^{\sim \gamma}(f)$ .

<sup>1</sup>For space reasons, proofs have been omitted from the published version of the paper; this full version includes all proofs in the appendix.

For instance, when optimizing is minimizing,  $CUT^{\min}$  is the set of optimal assignments (e.g., the cheapest cars);  $CUT^{\preceq \gamma}$  is the set of assignments satisfying the cut condition (e.g., the ‘cheap’ cars – those with a price  $\preceq \gamma$ ).

A representation language over  $\mathcal{X}$  w.r.t. a set  $\mathcal{V}$  is a set of data structures equipped with an interpretation function that associates with each of them a mapping  $f : D_{\mathcal{X}} \rightarrow \mathcal{V}$ . This mapping is called the *semantics* of the data structure, and the data structure is a *representation* of the mapping.

**Definition 2.1** (representation language; inspired from Gogic et al. (1995)). Given a valuation set  $\mathcal{V}$ , a *representation language*  $L$  over  $\mathcal{X}$  w.r.t.  $\mathcal{V}$ , is a 4-tuple  $\langle C_L, \text{Var}_L, f^L, s_L \rangle$ , where:

- $C_L$  is a set of data structures  $\alpha$  (also referred to as  $L$  representations or “formulæ”),
- $\text{Var}_L : C_L \rightarrow 2^{\mathcal{X}}$  is a scope function associating with each  $L$  representation the subset of  $\mathcal{X}$  it depends on,
- $f^L$  is an interpretation function associating with each  $L$  representation  $\alpha$  a mapping  $f_{\alpha}^L$  from the set of all assignments over  $\text{Var}_L(\alpha)$  to  $\mathcal{V}$ ,
- $s_L$  is a size function from  $C_L$  to  $\mathbb{N}$  that provides the size of any  $L$  representation.

Two formulæ (possibly from different languages) are equivalent iff they have the same scope and semantics.

**Valued decision diagrams.** In the following, we consider representation languages based on data structures called *valued decision diagrams*: such diagrams target the representation of  $\mathcal{V}$ -valued functions by allowing their arcs and nodes to be labeled with values in some set  $\mathcal{E}$  (generally,  $\mathcal{E} = \mathcal{V}$ , but as we will see, it is not always the case).

**Definition 2.2** (valued decision diagram). A *valued decision diagram* (VDD) over  $\mathcal{X}$  w.r.t.  $\mathcal{E}$  is a finite rooted DAG  $\alpha$ , of which every internal node  $N$  is labeled with a variable  $x \in \mathcal{X}$  and has a set  $\text{Out}(N)$  of  $|D_x|$  outgoing arcs, each arc  $a \in \text{Out}(N)$  being labeled with a distinct value  $v(a) \in D_x$ . Arcs and leaves can be labeled with elements of  $\mathcal{E}$ :  $\varphi(a)$  (resp.  $\varphi(L)$ ) denotes the label of arc  $a$  (resp. of leaf  $L$ ). The size of a decision diagram  $\alpha$ , denoted  $|\alpha|$ , is the size of the graph (its number of nodes and arcs) plus the sizes of all labels in it. VDD is the set of all valued decision diagrams.

In the following, we assume that the decision diagrams are *ordered*, i.e., a total, strict ordering  $\triangleright$  over  $\mathcal{X}$  is chosen, and for each path from the root to a leaf in a VDD  $\alpha$ , the associated sequence of internal node labels is required to be compatible w.r.t. this variable ordering (such diagrams are thus read-once). A path from the root to a leaf of  $\alpha$  represents a (possibly partial) assignment of  $\mathcal{X}$ . Note that the structure is deterministic: an assignment  $\vec{x}$  of  $\mathcal{X}$  corresponds to at most one path  $p_{\alpha}(\vec{x})$  in  $\alpha$ .

A VDD  $\alpha$  is *reduced* iff it does not contain any (distinct) isomorphic nodes.<sup>2</sup> A cache mechanism can be used

<sup>2</sup>Nodes  $N$  and  $M$  are isomorphic if they are labeled with the same variable  $x$ , and there exists a bijection  $B$  from  $\text{Out}(N)$  onto  $\text{Out}(M)$  such that  $\forall a \in \text{Out}(N)$ ,  $a$  and  $B(a)$  have the same end node, share the same value of  $D_x$ , and  $\varphi(a) = \varphi(B(a))$ .

to merge isomorphic nodes on the fly; this is why we assume in the following that the diagrams are reduced.

**VDD languages.** ADD, SLDD, and AADD are VDD languages, i.e., subsets of VDD. Each of them restricts the type of diagrams used (e.g., ADD allows  $\mathcal{E}$ -labels on leaves only), and defines how the diagram is to be interpreted. ADD, SLDD, and AADD thus differ syntactically (in the way diagrams are labeled) and semantically (in the way they are interpreted).

**Definition 2.3 (ADD).** ADD is the 4-tuple  $\langle C_{\text{ADD}}, \text{Var}_{\text{ADD}}, f^{\text{ADD}}, s_{\text{ADD}} \rangle$  where  $C_{\text{ADD}}$  is the set of ordered VDDs  $\alpha$  over  $\mathcal{X}$  such that leaves are labeled with elements of  $\mathcal{E} = \mathcal{V}$  (in general,  $\mathcal{E} = \mathcal{V} = \mathbb{R}^+$ ), the arcs are not labeled, and  $f^{\text{ADD}}$  is defined inductively as follows, for every assignment  $\vec{x}$ :

- if  $\alpha$  is a leaf node  $L$ , then  $f_{\alpha}^{\text{ADD}}(\vec{x}) = \varphi(L)$ ,
- otherwise, denoting  $N$  the root of  $\alpha$ ,  $x \in \mathcal{X}$  its label,  $d \in D_x$  such that  $\langle x, d \rangle \in \vec{x}$ ,  $a = \langle N, M \rangle$  the arc such that  $v(a) = d$ , and  $\beta$  the ADD formula rooted at node  $M$  in  $\alpha$ , then  $f_{\alpha}^{\text{ADD}}(\vec{x}) = f_{\beta}^{\text{ADD}}(\vec{x})$ .

In the AADD framework of Sanner and McAllester (2005), the co-domain of the represented functions is  $\mathcal{V} = \mathbb{R}^+$ , but only one (unlabeled) leaf is allowed and the arcs are labeled with pairs of values from  $\mathbb{R}^+$  (i.e.,  $\mathcal{E} = \mathbb{R}^+ \times \mathbb{R}^+ \neq \mathcal{V}$ ).

**Definition 2.4 (AADD).** AADD is the 4-tuple  $\langle C_{\text{AADD}}, \text{Var}_{\text{AADD}}, f^{\text{AADD}}, s_{\text{AADD}} \rangle$  where  $C_{\text{AADD}}$  is the set of ordered VDDs  $\alpha$  over  $\mathcal{X}$  with a unique leaf  $L$ , and the arcs of which are labeled with pairs  $\langle q, f \rangle$  in  $\mathbb{R}^+ \times \mathbb{R}^+$ . For normalization purposes, the root of  $\alpha$  is also equipped with a pair  $\langle q_0, f_0 \rangle$  from  $\mathbb{R}^+ \times \mathbb{R}^+$  (the “offset”). The semantics of the resulting VDD is given by, for every assignment  $\vec{x}$ ,  $f_{\alpha}^{\text{AADD}}(\vec{x}) = q_0 + (f_0 \times g_{\alpha}^{\text{AADD}}(\vec{x}))$ , where  $g_{\alpha}^{\text{AADD}}$  is defined inductively as follows:

- if  $\alpha$  is the leaf node  $L$ , then  $g_{\alpha}^{\text{AADD}}(\vec{x}) = 0$ ,
- otherwise, denoting  $N$  the root of  $\alpha$ ,  $x \in \mathcal{X}$  its label,  $d \in D_x$  such that  $\langle x, d \rangle \in \vec{x}$ ,  $a = \langle N, M \rangle$  the arc such that  $v(a) = d$ ,  $\varphi(a) = \langle q_a, f_a \rangle$ , and  $\beta$  the formula rooted at node  $M$  in  $\alpha$ , then  $g_{\alpha}^{\text{AADD}}(\vec{x}) = q_a + (f_a \times g_{\beta}^{\text{AADD}}(\vec{x}))$ .

The AADD framework is equipped with a normalization condition that makes (reduced) ordered AADD formulae canonical. Canonicity is important because it ensures a unique representation for each subformula, which is a key for efficiently recognizing and caching them.

In the SLDD $_{\otimes}$  framework (Wilson 2005),<sup>3</sup> arcs (but not leaves) are labeled with elements in  $\mathcal{E} = \mathcal{V}$ , and  $\langle \mathcal{E}, \otimes, 1_{\otimes} \rangle$  is assumed to be a commutative monoid:  $f_{\alpha}^{\text{SLDD}_{\otimes}}(\vec{x})$  is the aggregation by  $\otimes$  of the labels of the arcs along  $p_{\alpha}(\vec{x})$ .

**Definition 2.5 (SLDD $_{\otimes}$ ).** Given a commutative monoid  $\langle \mathcal{E}, \otimes, 1_{\otimes} \rangle$ , SLDD $_{\otimes}$  is the 4-tuple  $\langle C_{\text{SLDD}_{\otimes}}, \text{Var}_{\text{SLDD}_{\otimes}}, f^{\text{SLDD}_{\otimes}}, s_{\text{SLDD}_{\otimes}} \rangle$  where  $C_{\text{SLDD}_{\otimes}}$  is the set of ordered VDDs  $\alpha$  over  $\mathcal{X}$  with a unique leaf  $L$ , such that  $\varphi(L) = 1_{\otimes}$ , and the arcs of which are labeled with elements of  $\mathcal{E} = \mathcal{V}$ , and  $f^{\text{SLDD}_{\otimes}}$  is defined inductively as follows: for every assignment  $\vec{x}$ ,

<sup>3</sup>Note that our definition of SLDD differs from the original one in two ways: (i) we consider only ordered diagrams; and (ii) we use a commutative monoid instead of a commutative semiring, since the second operation of the semiring does not take part in the data structure (Fargier, Marquis, and Schmidt 2013).

- if  $\alpha$  is the leaf node  $L$ , then  $f_{\alpha}^{\text{SLDD}_{\otimes}}(\vec{x}) = 1_{\otimes}$ ,
- otherwise, denoting  $N$  the root of  $\alpha$ ,  $x \in \mathcal{X}$  its label,  $d \in D_x$  such that  $\langle x, d \rangle \in \vec{x}$ ,  $a = \langle N, M \rangle$  the arc such that  $v(a) = d$ , and  $\beta$  the SLDD $_{\otimes}$  formula rooted at node  $M$  in  $\alpha$ ,  $f_{\alpha}^{\text{SLDD}_{\otimes}}(\vec{x}) = \varphi(a) \otimes f_{\beta}^{\text{SLDD}_{\otimes}}(\vec{x})$ .

We add to the root of  $\alpha$  a value  $\varphi_0 \in \mathcal{E}$  (the “offset”). The augmented interpretation function of  $\alpha$  is  $f_{\alpha, \varphi_0} = \varphi_0 \otimes f_{\alpha}$ .

Two (ordered) monoids are particularly interesting, namely  $\langle \mathbb{R}^+, +, 0 \rangle$  (we call the corresponding language SLDD $_{+}$ ) and  $\langle \mathbb{R}^+, \times, 1 \rangle$  (we call the corresponding language SLDD $_{\times}$ ). Both SLDD $_{+}$  and SLDD $_{\times}$  formulae have a normalization condition that provides these languages with the canonicity property; moreover, any SLDD $_{+}$  or SLDD $_{\times}$  formula can be transformed in linear time into an equivalent AADD formula. This also holds for the ADD language, targeting any of the SLDD $_{+}$ , SLDD $_{\times}$ , or AADD languages. Last, note that when variables are Boolean and  $\mathcal{V} = \{0, 1\}$ , any ADD, SLDD $_{+}$ , SLDD $_{\times}$ , or AADD formula can be transformed in linear time into an equivalent OBDD, and (obviously) reciprocally.

### 3 Queries and Transformations

We now define many queries and transformations of interest in the general framework of representation languages w.r.t.  $\mathcal{V}$ , that is, they are meaningful even when  $\mathcal{V} \neq \mathbb{R}^+$ .

**Definition 3.1 (queries).** Let  $L$  denote a representation language over  $\mathcal{X}$  w.r.t. a set  $\mathcal{V}$  totally ordered by some  $\succeq$ .

- $L$  satisfies optimization **OPT $_{\max}$**  (resp. **OPT $_{\min}$** ) iff there exists a polynomial-time algorithm that maps every  $L$  formula  $\alpha$  to  $\max_{\vec{x} \in D_f} f_{\alpha}^L(\vec{x})$  (resp.  $\min_{\vec{x} \in D_f} f_{\alpha}^L(\vec{x})$ ).
- $L$  satisfies equivalence **EQ** iff there exists a polynomial-time algorithm that maps every pair of  $L$  formulae  $\alpha$  and  $\beta$  to 1 if  $f_{\alpha}^L = f_{\beta}^L$ , and to 0 otherwise.
- $L$  satisfies sentential entailment **SE** iff there exists a polynomial-time algorithm that maps every pair of  $L$  formulae  $\alpha$  and  $\beta$  to 1 if  $\forall \vec{x}, f_{\alpha}^L(\vec{x}) \succeq f_{\beta}^L(\vec{x})$ , and to 0 otherwise.
- $L$  satisfies partial upper (resp. lower, resp. level) consistency **CO $_{\succeq \gamma}$**  (resp. **CO $_{\preceq \gamma}$** , resp. **CO $_{\sim \gamma}$** ) iff there exists a polynomial-time algorithm that maps every  $\gamma \in \mathcal{V}$  and every  $L$  formula  $\alpha$  to 1 if  $\exists \vec{x}, f_{\alpha}^L(\vec{x}) \succeq \gamma$  (resp.  $f_{\alpha}^L(\vec{x}) \preceq \gamma$ , resp.  $f_{\alpha}^L(\vec{x}) \sim \gamma$ ) and to 0 otherwise.
- $L$  satisfies partial upper (resp. lower, resp. level) validity **VA $_{\succeq \gamma}$**  (resp. **VA $_{\preceq \gamma}$** , resp. **VA $_{\sim \gamma}$** ) iff there exists a polynomial-time algorithm that maps every  $\gamma \in \mathcal{V}$  and every  $L$  formula  $\alpha$  to 1 if  $\forall \vec{x}, f_{\alpha}^L(\vec{x}) \succeq \gamma$  (resp.  $f_{\alpha}^L(\vec{x}) \preceq \gamma$ , resp.  $f_{\alpha}^L(\vec{x}) \sim \gamma$ ) and to 0 otherwise.
- $L$  satisfies max-model enumeration **ME $_{\max}$**  iff there exists a polynomial  $p$  and an algorithm that outputs, for every  $L$  formula  $\alpha$ , all the elements of  $CUT^{\max}(f_{\alpha}^L)$  in time  $p(|\alpha|, |CUT^{\max}(f_{\alpha}^L)|)$ ,
- $L$  satisfies max-model counting **CT $_{\max}$**  iff there exists a polynomial-time algorithm that outputs, for every  $L$  formula  $\alpha$ , the number of elements in  $CUT^{\max}(f_{\alpha}^L)$ .

- L satisfies max-model extraction  $\mathbf{MX}_{\max}$  iff there exists a polynomial-time algorithm that maps every L formula  $\alpha$  to an element  $\vec{x}$  of  $CUT^{\max}(f_\alpha^L)$ .

We define  $\mathbf{ME}_{\min}$ ,  $\mathbf{ME}_{\succeq\gamma}$ ,  $\mathbf{ME}_{\preceq\gamma}$ ,  $\mathbf{ME}_{\sim\gamma}$  (resp.  $\mathbf{MX}_{\min}$ ,  $\mathbf{MX}_{\succeq\gamma}$ ,  $\mathbf{MX}_{\preceq\gamma}$ ,  $\mathbf{MX}_{\sim\gamma}$ ; resp.  $\mathbf{CT}_{\min}$ ,  $\mathbf{CT}_{\succeq\gamma}$ ,  $\mathbf{CT}_{\preceq\gamma}$ ,  $\mathbf{CT}_{\sim\gamma}$ ) in the same way as  $\mathbf{ME}_{\max}$  (resp.  $\mathbf{MX}_{\max}$ ; resp.  $\mathbf{CT}_{\max}$ ), using the sets  $CUT^{\min}(f_\alpha^L)$ ,  $CUT^{\succeq\gamma}(f_\alpha^L)$ ,  $CUT^{\preceq\gamma}(f_\alpha^L)$ ,  $CUT^{\sim\gamma}(f_\alpha^L)$  instead of  $CUT^{\max}(f_\alpha^L)$ .

The  $\mathbf{MX}$  and  $\mathbf{ME}$  families of queries are crucial in Bayesian reasoning and in interactive configuration. They capture for instance the computation of a (the) most probable explanation(s) ( $\mathbf{MX}_{\max}$ ), or cheapest configuration(s) ( $\mathbf{MX}_{\min}$ ); counting is also useful for such applications, e.g., for characterizing the number of cars that are ‘cheap’ ( $\mathbf{CT}_{\preceq\gamma}$ ), or the number of diseases that are likely enough to be considered ( $\mathbf{CT}_{\succeq\gamma}$ ). Other queries like consistency and validity, as well as  $\mathbf{EQ}$  and  $\mathbf{SE}$ , are useful for many reasoning problems (e.g., when pieces of information are encoded into weighted knowledge bases); they extend the corresponding queries defined for (Boolean) NNF formulæ.

**Definition 3.2** (transformations). Let L denote a representation language over  $\mathcal{X}$  w.r.t.  $\mathcal{V}$ , and  $\odot$  an associative and commutative binary operator over  $\mathcal{V}$ .

- L satisfies conditioning  $\mathbf{CD}$  iff there exists a polynomial-time algorithm that maps every L formula  $\alpha$ , every  $X \subseteq \mathcal{X}$ , and every  $\vec{x} \in D_X$  to an L representation of  $f_{\alpha, \vec{x}}^L$ .
- L satisfies bounded  $\odot$ -combination  $\odot\mathbf{BC}$  iff there exists a polynomial-time algorithm that maps every pair of L formulæ  $\alpha$  and  $\beta$  to an L representation of  $f_\alpha^L \odot f_\beta^L$ .
- L satisfies  $\odot$ -combination  $\odot\mathbf{C}$  iff there exists a polynomial-time algorithm that maps every set of L formulæ  $\{\alpha_1, \dots, \alpha_n\}$  to an L representation of  $\odot_{i=1}^n f_{\alpha_i}^L$ .
- L satisfies variable (resp. single variable)  $\odot$ -elimination  $\odot\mathbf{Elim}$  (resp.  $\mathbf{S}\odot\mathbf{Elim}$ ) iff there exists a polynomial-time algorithm that maps every L formula  $\alpha$  and every subset  $X \subseteq \mathcal{X}$  of variables (resp every singleton  $X \subseteq \mathcal{X}$ ) to an L representation of  $\odot_{x \in X} \odot_{\vec{x} \in D_x} f_{\alpha, \vec{x}}^L$ .
- L satisfies single bounded-variable  $\odot$ -elimination  $\mathbf{SB}\odot\mathbf{Elim}$  iff there exists a polynomial  $p$  and an algorithm that maps every L formula  $\alpha$  and every  $x \in \mathcal{X}$  to an L representation of  $\odot_{\vec{x} \in D_x} f_{\alpha, \vec{x}}^L$  in time  $p(|\alpha|^{D_x})$ .
- L satisfies single variable  $\odot$ -marginalization ( $\odot\mathbf{Marg}$ ) iff there exists a polynomial-time algorithm that maps every L formula  $\alpha$  and every  $x \in \mathcal{X}$  to an L representation of  $\odot_{y \in \mathcal{X} \setminus \{x\}} \odot_{\vec{y} \in D_y} f_{\alpha, \vec{y}}^L$ .
- L satisfies  $\gamma$ -cut up  $\mathbf{CUT}_{\succeq\gamma}$ , w.r.t. a preorder  $\succeq$  on  $\mathcal{V}$ , iff there exists a polynomial-time algorithm that maps every L formula  $\alpha$  and every  $\gamma, a, b \in \mathcal{V}$  such that  $a \succ b$ , to an L representation of the function  $g$  defined by  $g(\vec{x}) = a$  if  $\vec{x} \in CUT^{\succeq\gamma}(f_\alpha)$ , and  $g(\vec{x}) = b$  otherwise.

We define  $\mathbf{CUT}_{\preceq\gamma}$ ,  $\mathbf{CUT}_{\sim\gamma}$ ,  $\mathbf{CUT}_{\max}$ , and  $\mathbf{CUT}_{\min}$  in the same way, from the sets  $CUT^{\preceq\gamma}(f_\alpha)$ ,  $CUT^{\sim\gamma}(f_\alpha)$ ,  $CUT^{\max}(f_\alpha)$ , and  $CUT^{\min}(f_\alpha)$ , respectively.

The classical forgetting of variables corresponds to their max-elimination. Single variable marginalization is equivalent to the elimination of all variables but one. For instance, sum-marginalization is important in Bayes nets for achieving the posterior marginal request (see Darwiche (2009)); and in configuration problems, the min-marginalization on a variable in a VDD formula representing some price function amounts to computing the minimal price associated with each possible value of this variable (an option, in the car example; see Astesana, Cosserrat, and Fargier (2010)). Another example is value iteration in stochastic planning, which can be performed via a sequence of sum-eliminations and  $\times$ -combinations on ADD formulæ (Hoey et al. 1999).

Finally, the family of cuts captures the restriction of the function to the optimal assignments (e.g., the cheapest cars, the most probable explanations) or to those that are good enough (cars cheaper than  $\gamma$  euros, diseases with a probability greater than  $\gamma$ ). For generality, we defined this request as a transformation within the language, but choosing  $a = 1$  and  $b = 0$  when  $\mathcal{V} = \mathbb{R}^+$ , cutting a VDD results in an MDD; the MDD language (Srinivasan et al. 1990) is a direct extension of OBDD to non-Boolean variables, and it satisfies roughly the same queries and transformations as OBDD, e.g.,  $\mathbf{CO}$ ,  $\mathbf{CD}$ ,  $\mathbf{SE}$ , etc. (Amilhastre et al. 2012).

## 4 A KC Map for Ordered $\mathbb{R}^+$ -VDDs

We can now draw the map of existing ordered VDD languages representing functions from  $\mathcal{X}$  to  $\mathbb{R}^+$ , namely ADD, SLDD $_{\times}$ , SLDD $_{+}$ , and AADD. That is, we focus in the following on  $\mathcal{V} = \mathbb{R}^+$ , with  $\succeq$  being the usual  $\geq$  order on  $\mathbb{R}^+$ , and  $\odot \in \{\max, \min, +, \times\}$ . We refrain from explicitly investigating SLDD $_{\max}$  and SLDD $_{\min}$ , since it has been shown that these languages are equivalent, up to a polynomial transformation, to ADD (Fargier, Marquis, and Schmidt 2013).

Each of the four languages satisfies  $\mathbf{CD}$ : conditioning a formula  $\alpha$  by an assignment  $\vec{x}$  can be achieved in linear time using an algorithm similar to the OBDD one. Also, since the representation of any function as a reduced ADD (resp. reduced and normalized AADD, SLDD $_{\times}$ , SLDD $_{+}$ ) is unique, and the reduction and normalization procedures of each language are polynomial-time,  $\mathbf{EQ}$  is satisfied by each of the four languages. Finally,  $\mathbf{SE}$  is satisfied by ADD, SLDD $_{+}$ , and SLDD $_{\times}$ , using a combination of cuts, inversion of labels, and translation to MDD. Whether AADD satisfies  $\mathbf{SE}$  remains open.

**Proposition 4.1.**

- ADD, SLDD $_{+}$ , SLDD $_{\times}$ , and AADD satisfy  $\mathbf{EQ}$  and  $\mathbf{CD}$ .
- ADD, SLDD $_{+}$ , and SLDD $_{\times}$  satisfy  $\mathbf{SE}$ .

Let us now draw the KC map for the requests identified in the previous section. We distinguish three classes of requests: those related to optimization, that are tractable on the VDD languages; those related to cutting with respect to some threshold  $\gamma$ , that may become harder; and the combination and projection transformations.

**Requests related to optimization tasks.** The results we obtained are summarized in Table 1. As already mentioned,  $\mathbf{OPT}_{\max}$  and  $\mathbf{OPT}_{\min}$  have been shown to be tractable for

Table 1: Results about basic queries, optimization, and  $\gamma$ -cutting;  $\checkmark$  means “satisfies”,  $\bullet$  means “does not satisfy”, and  $\circ$  means “does not satisfy unless  $P = NP$ ”. Results for additive valued constraint satisfaction problems ( $VCSP_+$ ) are given here as a baseline.

Query	ADD	SLDD $_+$	SLDD $\times$	AADD	VCSP $_+$
CD	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$
EQ	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	?
SE	$\checkmark$	$\checkmark$	$\checkmark$	?	$\circ$
OPT $_{\max}$ / OPT $_{\min}$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\circ$
CT $_{\max}$ / CT $_{\min}$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\circ$
ME $_{\max}$ / ME $_{\min}$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\circ$
MX $_{\max}$ / MX $_{\min}$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\circ$
CUT $_{\max}$ / CUT $_{\min}$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	?
VA $_{\sim\gamma}$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	?
VA $_{\succeq\gamma}$ / VA $_{\preceq\gamma}$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\circ$
CO $_{\sim\gamma}$	$\checkmark$	$\circ$	$\circ$	$\circ$	$\circ$
CO $_{\succeq\gamma}$ / CO $_{\preceq\gamma}$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\circ$
ME $_{\sim\gamma}$	$\checkmark$	$\circ$	$\circ$	$\circ$	$\circ$
ME $_{\succeq\gamma}$ / ME $_{\preceq\gamma}$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\circ$
MX $_{\sim\gamma}$	$\checkmark$	$\circ$	$\circ$	$\circ$	$\circ$
MX $_{\succeq\gamma}$ / MX $_{\preceq\gamma}$	$\checkmark$	$\checkmark$	$\checkmark$	$\checkmark$	$\circ$
CUT $_{\sim\gamma}$	$\checkmark$	$\circ$	$\circ$	$\circ$	?
CUT $_{\succeq\gamma}$ / CUT $_{\preceq\gamma}$	$\checkmark$	$\bullet$	$\bullet$	$\bullet$	?
CT $_{\sim\gamma}$	$\checkmark$	$\circ$	$\circ$	$\circ$	$\circ$
CT $_{\succeq\gamma}$ / CT $_{\preceq\gamma}$	$\checkmark$	$\circ$	$\circ$	$\circ$	$\circ$

normalized AADD formulæ (Sanner and McAllester 2005), for SLDD formulæ (Wilson 2005), and their satisfaction is obvious for ADD. All tractability results of Table 1 can be related to the fact that (i) VDDs are circuit-free graphs, and (ii) the aggregation of the  $\varphi$  values is monotonic in the classes considered. In such diagrams, minimal (resp. maximal) paths can be obtained in polynomial time, thanks to a shortest (resp. longest) path algorithm; this is the basis for a polynomial-time procedure that builds an MDD formula representing the optimal assignments, and hence implies the satisfaction of CUT $_{\max}$  and CUT $_{\min}$ .

**Proposition 4.2.** ADD, SLDD $_+$ , SLDD $\times$ , and AADD satisfy OPT $_{\max}$ , OPT $_{\min}$ , CUT $_{\max}$ , CUT $_{\min}$ , ME $_{\max}$ , ME $_{\min}$ , MX $_{\max}$ , MX $_{\min}$ , CT $_{\max}$ , and CT $_{\min}$ .

**Requests related to  $\gamma$ -cuts.** Requests related to pure optimization are easy to solve. However, optimization alone is not sufficient for many applications; for instance, a customer looking for a car is not always interested in finding out one of the cheapest cars: a ‘cheap’ car (i.e., with a cost lower than a given threshold) may be more interesting than the cheapest ones if it fulfills other desiderata of the customer. Hence the importance of requests related to  $\gamma$ -cuts.

Fortunately, comparing the maximal (resp. minimal) value of  $f_\alpha^L$  (which can be computed in polynomial time) to some  $\gamma \in \mathcal{V}$  is enough to decide whether there exists an  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \succeq \gamma$  (resp.  $\preceq \gamma$ ). Similarly, deciding the  $\gamma$ -validity of a formula  $\alpha$  only requires the comparison of  $\gamma$  to the maximal (resp. minimal) value of  $f_\alpha^L$ .

**Proposition 4.3.** ADD, SLDD $_+$ , SLDD $\times$ , and AADD satisfy CO $_{\succeq\gamma}$ , VA $_{\succeq\gamma}$ , CO $_{\preceq\gamma}$ , VA $_{\preceq\gamma}$ , and VA $_{\sim\gamma}$ .

Although checking that there is an assignment that leads to a valuation greater than or equal to  $\gamma$  is polynomial, deciding whether there exists an assignment leading *exactly* to  $\gamma$  is not tractable for SLDD $_+$ , SLDD $\times$ , and AADD. The proof is based on a polynomial reduction from the SUBSET SUM decision problem (Garey and Johnson 1979).

**Proposition 4.4.** SLDD $_+$ , SLDD $\times$ , and AADD do not satisfy CO $_{\sim\gamma}$  unless  $P = NP$ .

It is quite clear that the satisfaction of MX $_{\sim\gamma}$  or CT $_{\sim\gamma}$  is a sufficient condition to that of CO $_{\sim\gamma}$ ; and this holds in all generality, not only for our four  $\mathbb{R}^+$ -valued languages. Similarly, it can be shown that ME $_{\sim\gamma}$  is a sufficient condition for CO $_{\sim\gamma}$ , and that CUT $_{\sim\gamma}$  implies MX $_{\sim\gamma}$  as long as one of the MX queries is satisfied.

**Proposition 4.5.** Let  $L$  be a representation language over  $\mathcal{X}$  w.r.t.  $\mathcal{V}$ , where  $\mathcal{V}$  is totally ordered by a relation  $\succeq$ .

- If  $L$  satisfies both CUT $_{\sim\gamma}$  and one of the MX queries, then it satisfies MX $_{\sim\gamma}$ .
- If  $L$  satisfies MX $_{\sim\gamma}$ , CT $_{\sim\gamma}$ , or ME $_{\sim\gamma}$ , then it satisfies CO $_{\sim\gamma}$ .

**Corollary 4.6.** SLDD $_+$ , SLDD $\times$ , and AADD do not satisfy MX $_{\sim\gamma}$ , CUT $_{\sim\gamma}$ , CT $_{\sim\gamma}$ , or ME $_{\sim\gamma}$  unless  $P = NP$ .

Thus, except for VA $_{\sim\gamma}$ , queries about a precise cut of the function represented by an arc-labeled (in the wide sense) VDD are intractable. The situation is more nuanced when lower and upper cuts are considered instead.

**Proposition 4.7.** Let  $L \in \{\text{SLDD}_+, \text{SLDD}_\times, \text{AADD}\}$ .

- $L$  satisfies ME $_{\succeq\gamma}$ , ME $_{\preceq\gamma}$ , MX $_{\succeq\gamma}$ , and MX $_{\preceq\gamma}$ .
- $L$  does not satisfy CT $_{\succeq\gamma}$  or CT $_{\preceq\gamma}$  unless  $P = NP$ .
- $L$  does not satisfy CUT $_{\succeq\gamma}$  or CUT $_{\preceq\gamma}$ .

For proving the first item, the basic idea is that from any node of a normalized AADD formula, there is a path to the leaf that has value 1, and a path to the leaf that has value 0. A path from the root to this node gives it an offset, gathered from its arcs, say  $\langle p, q \rangle$ . Hence we can enumerate all paths, without exploring nodes for which  $p + q < \gamma$  for ME $_{\succeq\gamma}$  (resp.  $p > \gamma$  for ME $_{\preceq\gamma}$ ).

The proof of the next item comes from that fact that if CT $_{\succeq\gamma}$  held, then we could count the assignments  $\vec{x}$  such that  $f_\alpha(\vec{x}) \geq \gamma$ , and also those for which  $f_\alpha(\vec{x}) > \gamma$ ; that is, we could satisfy CT $_{\sim\gamma}$ , which has been shown intractable (and similarly for CT $_{\preceq\gamma}$ ).

The intractability of CUT $_{\succeq\gamma}$  or CUT $_{\preceq\gamma}$  is unconditional, and the proofs are trickier. For the specific case of CUT $_{\succeq\gamma}$  on SLDD $_+$ , the proof uses the fact that the function  $f(\vec{y} \cdot \vec{z}) = \sum_{i=1}^n y_i \cdot 2^{n-i} + \sum_{i=1}^n z_i \cdot 2^{n-i}$  can be represented as an SLDD $_+$  formula of  $2n + 1$  nodes only, using the variable ordering  $y_1 \triangleleft \dots \triangleleft y_n \triangleleft z_1 \triangleleft \dots \triangleleft z_n$ , whereas there is no polynomial-size OBDD $_{\triangleleft}$  representation of the function returning 1 when  $f(\vec{y} \cdot \vec{z}) \geq 2^n$  and 0 otherwise. The other proofs are similar, using well-chosen functions instead of  $f$ .

These results about AADD and SLDD contrast with the case of ADD, which satisfies each CUT transformation ( $\gamma$ -cuts

are obtained thanks to a simple leaf-merging procedure), and thus satisfies each of the **MX**, **ME**, **CT**, and **CO** queries.

**Combination, variable elimination, marginalization.**

None of the VDD languages considered in this paper satisfies the unbounded combination or unbounded variable elimination transformations; this result is not very surprising, observing that (i) unbounded disjunctive combination (**VC**) and forgetting (**FO**) are not satisfied by OBDD; (ii) any OBDD formula can be viewed as an ADD formula, and thus be translated in polynomial time into an  $SLDD_+$  (resp.  $SLDD_\times$ , AADD) formula; and (iii) the disjunction of several OBDD formulæ amounts to cutting their  $+$ -combination at the minimal level ( $CUT_{\min}$  is satisfied): by construction, the countermodels of the disjunction are the  $\vec{x}$  such that  $\varphi(\vec{x}) = 0$ , so the resulting formula can be viewed as an OBDD formula the negation of which is equivalent to the disjunction of the original formulæ). The other proofs are based on similar arguments. Note that contrary to the OBDD case, even  $\ominus$ -eliminating a single variable is hard (roughly speaking, the  $\ominus$ -combination of two formulæ can be built by  $\ominus$ -eliminating an additional variable).

**Proposition 4.8.** *For any  $L \in \{ADD, SLDD_+, SLDD_\times, AADD\}$  and any  $\odot \in \{\max, \min, +, \times\}$ ,  $L$  does not satisfy  $\odot C$ ,  $\odot Elim$ , or  $S\odot Elim$ .*

More difficult is the question of bounded combination. An extension of “apply” algorithm of Bryant (1986), which is polynomial-time on OBDD structures for the AND and OR operators, has been proposed (Sanner and McAllester 2005) to compute the combination (e.g., by  $+$ ,  $\times$ ,  $\max$ ,  $\min$ ) of two AADD formulæ; it can be easily adapted to the other VDD languages considered in this paper. However, the complexity of this “apply” algorithm had not been formally identified. One of the main results of this paper is that bounded combinations are not tractable for AADD, which implies that the extended “apply” is not a polynomial-time algorithm.

**Proposition 4.9.**

- $SLDD_+$ ,  $SLDD_\times$ , and AADD do not satisfy  $\max BC$ ,  $\min BC$ ,  $SB\max Elim$ , or  $SB\min Elim$ .
- $SLDD_\times$  and AADD do not satisfy  $+BC$  or  $SB+Elim$ .
- $SLDD_+$  and AADD do not satisfy  $\times BC$  or  $SB\times Elim$ .

The difficulty of  $\max BC$  or  $\min BC$  comes from that of  $CUT_{\geq \gamma}$  and  $CUT_{\leq \gamma}$ . We show the difficulty of  $\times BC$  by considering the two following functions on Boolean variables:  $f(\vec{x}) = \sum_{i=0}^{n-1} x_i \cdot 2^i$  (representation of an integer by a bitvector) and  $g(\vec{x}) = 2^{n+1} - f(\vec{x})$ ; each can be represented as an  $SLDD_+$  formula (with ordering  $x_0 \triangleleft x_1 \triangleleft \dots \triangleleft x_{n-1}$ ) with  $n + 1$  nodes and  $2n$  arcs. Then we can show that the  $SLDD_+$  representation of  $f \times g$  (using the same variable ordering) contains an exponential number of terminal arcs, even if all nodes at the last level have been normalized; this proves that each ordered  $SLDD_+$  representation of  $f \times g$  is of exponential size. The other proofs are similar, using well-chosen functions  $f$  and  $g$ .

There are actually only two cases in which bounded combinations are tractable: when the language considered is

Table 2: Results about transformations (legend in Table 1).

Transformation	ADD	$SLDD_+$	$SLDD_\times$	AADD
$\max BC / \min BC$	✓	•	•	•
$+BC$	✓	✓	•	•
$\times BC$	✓	•	✓	•
$\max C / \min C$	•	•	•	•
$+C / \times C$	•	•	•	•
$\max Elim / \min Elim$	•	•	•	•
$+Elim / \times Elim$	•	•	•	•
$S\max Elim / S\min Elim$	•	•	•	•
$S+Elim / S\times Elim$	•	•	•	•
$SB\max Elim / SB\min Elim$	✓	•	•	•
$SB+Elim$	✓	✓	•	•
$SB\times Elim$	✓	•	✓	•
$\max Marg / \min Marg$	✓	✓	✓	✓
$+Marg$	✓	✓	✓	✓
$\times Marg$	✓	?	✓	?

ADD, and when the combination operator is also the one that aggregates arc values in the language considered (i.e., addition for  $SLDD_+$ , and product for  $SLDD_\times$ ). In these cases, it is also polynomial to eliminate a single variable with a bounded domain, by definition of variable elimination.

**Proposition 4.10.**

- $SLDD_+$  satisfies  $+BC$  and  $SB+Elim$ .
- $SLDD_\times$  satisfies  $\times BC$  and  $SB\times Elim$ .
- ADD satisfies  $\odot BC$  and  $SB\odot Elim$ , for any operation  $\odot \in \{\times, +, \min, \max\}$ .

Finally, we have proved that  $\times$ -marginalization is tractable for ADD and  $SLDD_\times$ , and that  $\max$ -marginalization,  $\min$ -marginalization and  $+$ -marginalization are tractable for all languages considered. Whether AADD and  $SLDD_+$  satisfy  $\times$ -marginalization remains an open question.

## 5 Conclusion

In this paper, we presented a complexity analysis of VDD languages, based on a set of queries and transformations of interest. Requests related to optimization appear as tractable for all VDD languages, as well as additive marginalization and some of the queries related to cutting. Cutting queries prove computationally difficult when a level  $\gamma$  is has to be reached exactly, and this is also the case for the transformations we considered. One of the main results of the paper is that bounded combinations are not tractable on VDD languages, which implies that no “apply” algorithm can run in polynomial time on AADD formulæ in the general case (this is the case even for simple “Boolean-style” operations such as  $\min$  and  $\max$ ). When bounded additive (resp. multiplicative) combination is required to be achieved efficiently, the time/space tradeoff offered by  $SLDD_+$  (resp.  $SLDD_\times$ ) is valuable. Finally, it turns out that the complexity of the various queries related to optimization is better for the VDD languages than for other languages dedicated to the representation of non-Boolean functions, such as VCSP $_+$  (and similar results are expected for GAI nets or Bayes nets, for which optimization is also hard).

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## Appendix: Proofs

### Preliminaries

We first introduce a couple of technical propositions that are only evoked in the main text, for space reasons, but are useful for proving the following results. For simplicity, we often consider that the offset of SLDD and AADD formulæ is the valuation of a pseudo-arc pointing to the root, called the “offset-arc”.

**Proposition A.1.** *Any VDD formula can be turned in polynomial time into an equivalent reduced formula in the same language.*

*Proof.* The proof is straightforwardly adapted from the well-known bottom-up reduction algorithm of OBDD (Bryant 1986). The goal of the reduction procedure is to merge the isomorphic nodes (two nodes  $N$  and  $N'$  in a VDD formula are isomorphic when they are leaf nodes with the same label, or when they are internal nodes labeled with the same variable  $x$ , have the same number  $k$  of outgoing arcs  $a_1, \dots, a_k$  and  $a'_1, \dots, a'_k$  and for each  $i \in \{1, \dots, k\}$ ,  $v(a_i) = v(a'_i)$  and  $\varphi(a_i) = \varphi(a'_i)$ ) and to remove the redundant nodes (an internal node  $N$  is redundant in a VDD formula when it has a single child  $M$  and every arc from  $N$  to  $M$  has the same  $\varphi$ -label). It simply traverses the diagram while maintaining a cache of already encountered nodes, recording their variable and the label, value, and destination of each of their outgoing arcs. When it finds a node  $N$  that is isomorphic to some node  $N'$  in the cache, it removes the duplicate  $N$ , redirecting all incoming arcs to  $N'$ . When it finds a node  $N$  with all outgoing arcs pointing to the same node  $M$  and having the same valuation, it removes  $N$  and redirects its incoming arcs to  $M$ , updating their valuations accordingly (this update operation depends on the language under consideration).  $\square$

**Definition A.2.** An AADD formula  $\alpha$  is normalized if and only if

---

**Algorithm 1:** NormalizeAADD( $\alpha$ )

---

input : an AADD formula  $\alpha$   
output: a normalized AADD formula equivalent to  $\alpha$

- 1 foreach arc  $a$  pointing to the leaf do  $f_a := 0$
- 2 foreach non-leaf node  $N$  of  $\alpha$  in reverse topological ordering do
  - 3  $q_{min} := \min_{a \in \text{Out}(N)} q_a$
  - 4  $range := \max_{a \in \text{Out}(N)} (q_a + f_a) - q_{min}$
  - 5 foreach  $a' \in \text{In}(N)$  do
    - 6  $q_{a'} := q_{a'} + q_{min} \times f_{a'}$
    - 7  $f_{a'} := f_{a'} \times range$
    - 8 if  $f_{a'} = 0$  then
    - 9  $\quad$  redirect  $a'$  to the leaf
  - 10 if  $range = 0$  then
  - 11  $\quad$  remove  $N$  and its outgoing arcs (the incoming arcs have been redirected)
  - 12 else
  - 13  $\quad$  foreach  $a \in \text{Out}(N)$  do
    - 14  $\quad \quad q_a := (q_a - q_{min}) / range$
    - 15  $\quad \quad f_a := f_a / range$
- 16 return  $\alpha$

---

- for each node  $N$  in  $\alpha$ ,  $\min_{a \in \text{Out}(N)} q_a = 0$ ;
- for each node  $N$  in  $\alpha$ ,  $\max_{a \in \text{Out}(N)} (q_a + f_a) = 1$ ;
- for any arc  $a$  (including the offset-arc),  $f_a = 0$  if and only if  $a$  points to the leaf.

**Proposition A.3.** *There exists a linear algorithm transforming any AADD formula into an equivalent normalized AADD formula.*

*Proof.* The backward normalization procedure of AADD is outlined in Algorithm 1 (note that the offset-arc is considered as a normal arc, pointing to the root). It should be clear that (i) the processing of each node leaves its semantics identical (modifications of outgoing arcs are reported on incoming arcs); (ii) each node is modified so as to obey the normalization conditions; and (iii) each arc is modified at most two times, so the procedure is linear in the number of arcs in  $\alpha$ .  $\square$

**Definition A.4.** An SLDD<sub>+</sub> (resp. SLDD<sub>×</sub>) formula  $\alpha$  is normalized if and only if each node  $N$  in  $\alpha$  verifies  $\min_{a \in \text{Out}(N)} \varphi(a) = 0$  (resp.  $\max_{a \in \text{Out}(N)} \varphi(a) = 1$ ).

**Proposition A.5.** *There exists a linear algorithm transforming any SLDD<sub>+</sub> (resp. SLDD<sub>×</sub>) formula into an equivalent normalized SLDD<sub>+</sub> (resp. SLDD<sub>×</sub>) formula.*

*Proof.* The backward normalization procedure for SLDD<sub>+</sub> and SLDD<sub>×</sub> is outlined in Algorithm 2 (note that the offset-arc is considered as a normal arc, pointing to the root).  $\square$

There is no normalization condition for ADD; to simplify the statement of the following proposition, we implicitly consider that a “normalized ADD formula” is simply an ADD formula.

---

**Algorithm 2:** NormalizeSLDD( $\alpha$ )

---

input : an SLDD formula  $\alpha$   
output: a normalized SLDD formula equivalent to  $\alpha$   
// For SLDD<sub>+</sub>, let  $\otimes$  be  $+$ ,  $\otimes^{-1}$  be  $-$ , and proj be min  
// For SLDD<sub>×</sub>, let  $\otimes$  be  $\times$ ,  $\otimes^{-1}$  be  $\div$ , and proj be max

- 1 foreach non-leaf node  $N$  of  $\alpha$  in reverse topological ordering do
  - 2  $\varphi_m := \text{proj}_{a \in \text{Out}(N)} \varphi(a)$   
// Rem: when proj is max  
//  $\varphi_m = 0$  iff  $\forall a, \varphi(a) = 0$
  - 3 foreach  $a \in \text{Out}(N)$  do
    - 4 if  $\varphi(a) = \varphi_m$  then
    - 5  $\quad \varphi(a) := 1_{\otimes}$
    - 6 else  $\varphi(a) := \varphi(a) \otimes^{-1} \varphi_m$
  - 7
  - 8 foreach  $a \in \text{In}(N)$  do
  - 9  $\quad \varphi(a) := \varphi(a) \otimes \varphi_m$
- 10 return  $\alpha$

---

**Proposition A.6.** *For any  $L \in \{\text{ADD}, \text{SLDD}_+, \text{SLDD}_\times, \text{AADD}\}$  formula  $\alpha$ ,*

- *there is a unique (up to isomorphism) reduced and normalized L representation  $\alpha'$  of  $f_\alpha^L$ ;*
- *no L representation of  $f_\alpha^L$  can be smaller than  $|\alpha'|$ .*

*Proof.* This has been proven by Bahar et al. (1993) for ADD, Wilson (2005) for SLDD<sub>+</sub> and SLDD<sub>×</sub>, and Sanner and McAllester (2005) for AADD.  $\square$

**Proposition A.7.** *Denoting  $L \geq_\ell L'$  the fact that there exists a linear-time algorithm that associates with any L formula an equivalent L' formula, the following results hold:*

- $\text{ADD} \geq_\ell \text{SLDD}_+ \geq_\ell \text{AADD}$ ;
- $\text{ADD} \geq_\ell \text{SLDD}_\times \geq_\ell \text{AADD}$ .

*Proof.*

- $\text{SLDD}_+ \geq_\ell \text{AADD}, \text{SLDD}_\times \geq_\ell \text{AADD}$ : Because any SLDD<sub>+</sub> (resp. SLDD<sub>×</sub>) representation of a function  $f$  can be transformed in linear time into an AADD representation  $\alpha$  of the same function: for each arc  $a$  (including the offset-arc), its valuation  $\varphi(a)$  becomes  $\langle \varphi(a), 1 \rangle$  (resp.  $\langle 0, \varphi(a) \rangle$ ); the initial offset of  $\alpha$  is  $\langle q_0, 1 \rangle$ .
- $\text{ADD} \geq_\ell \text{SLDD}_\times, \text{ADD} \geq_\ell \text{SLDD}_+, \text{ADD} \geq_\ell \text{AADD}$ : any ADD representation of a function  $f$  can be transformed in linear time into an SLDD<sub>+</sub> (resp. SLDD<sub>×</sub>, resp. AADD) representation of the same function; the transformation process is as follows: each arc  $a$  entering a terminal node  $N$  receives the valuation  $\varphi(a) = \varphi(N)$  (resp.  $\varphi(a) = \varphi(N)$ , resp.  $\langle \varphi(N), 0 \rangle$ ); the other arcs are labeled with the neutral element of the language considered (0 for SLDD<sub>+</sub>, 1 for SLDD<sub>×</sub>,  $\langle 0, 1 \rangle$  for AADD); the labels of the terminal nodes are deleted and those nodes are merged so as to become the leaf of the resulting VDD formula. The offset is 0 (resp. 1, resp.  $\langle 0, 1 \rangle$ ).  $\square$

**Proposition A.8.** Any ADD, SLDD<sub>+</sub>, SLDD<sub>×</sub>, or AADD representation of a function taking its values in  $\mathcal{V} = \{0, 1\}$  can be translated in polynomial time into an MDD representation of the same function.

*Proof.* We will show the result for AADD; the others can be inferred from it, since any formula in one of these languages can be transformed in linear time into an equivalent AADD formula (Proposition A.7). Note however that the result is actually obvious for the ADD language: when  $\mathcal{V} = \mathcal{E} = \{0, 1\}$ , ADD = MDD.

Any AADD formula can be normalized in linear time (Proposition A.3). Let  $\alpha$  be a normalized AADD representation of a function taking its values in  $\mathcal{V} = \{0, 1\}$ . When  $\alpha$  has no non-leaf node, it represents a constant function, so the translation to MDD is trivial. Suppose  $\alpha$  contains at least one non-leaf node; we show by induction that all arcs that do not point to the leaf (including the offset-arc) have valuation  $\langle 0, 1 \rangle$ .

Suppose that this is true for all arcs on some path from the offset-arc to a node  $N$ . Let  $a \in \text{Out}(N)$  be an arc that does not point to the leaf. Denoting  $M$  the destination node of  $a$ , the normalization condition also ensures that there exists one path from  $M$  to the leaf that has value 0, and another that has value 1. All in all, there exists a complete path of value  $q_a$  and another complete path of value  $q_a + f_a$ ; these two values must be equal to 0 or 1. Since by the normalization condition,  $f_a \neq 0$ , the only possibility is that  $q_a = 0$  and  $f_a = 1$ . This reasoning clearly applies to the offset arc, so by induction, all arcs not pointing to the leaf have valuation  $\langle 0, 1 \rangle$ .

Now, let  $a$  be an arc pointing to the leaf. By the normalization condition,  $f_a = 0$ . Any complete path traversing  $a$  has value  $q_a$  (recall that along any path, all arcs except the last one have valuation  $\langle 0, 1 \rangle$ ), so  $q_a$  is either 0 or 1.

The conversion to MDD is thus direct: create a new leaf labeled with 1, redirect the  $\langle 1, 0 \rangle$  arcs to this new leaf, and remove all the arc valuations.  $\square$

## Queries

**Proposition A.9.** ADD, SLDD<sub>+</sub>, SLDD<sub>×</sub>, and AADD satisfy EQ.

*Proof.* This query is satisfied since each language L in  $\{\text{ADD}, \text{SLDD}_+, \text{SLDD}_\times, \text{AADD}\}$  offers the canonicity property, i.e., any function has a unique reduced, normalized L representation given a fixed variable ordering (Proposition A.6), and since normalizing and reducing an L formula can be done in linear time (Proposition A.1 for the reduction, Proposition A.3 for the normalization of AADD, and Proposition A.5 for the normalization of SLDD).  $\square$

**Proposition A.10.** ADD, SLDD<sub>+</sub>, SLDD<sub>×</sub>, and AADD satisfy CD.

*Proof.* We first show that single variable conditioning is in linear time on VDD formulæ. Assume that the conditioning of  $\alpha$  by  $x = v$  is asked for. For any node  $N$  labeled with  $x$ , the arcs in  $\text{Out}(N)$  corresponding to a value different from  $v$

must be discarded to get a VDD representation of the restriction  $f_{\alpha, \langle x, v \rangle}$  of  $f_\alpha$ . Because VDD formulæ are deterministic, there is only one arc  $a$  in  $\text{Out}(N)$  corresponding to value  $v$ .

In order to derive a VDD representation of  $f_{\alpha, \langle x, v \rangle}$ , each arc  $b$  in  $\text{In}(N)$  has to be redirected to the destination node of  $a$  and its label must be updated to take account of the removal of  $a$ . This update depends on the language considered: for ADD, there is nothing to do; for AADD, the label  $\langle q_b, f_b \rangle$  of  $b$  must be replaced by  $\langle q_b + f_b \times q_a, f_b \times f_a \rangle$ ; for SLDD<sub>+</sub> (resp. SLDD<sub>×</sub>), the label  $\varphi_b$  of  $b$  must be replaced by  $\varphi_b + \varphi_a$  (resp.  $\varphi_b \times \varphi_a$ ).

Accordingly, single variable conditioning can be achieved in time linear in the size of  $\alpha$ , and the resulting formula has strictly fewer nodes and arcs than  $\alpha$  (each node processing removes  $|D_x| - 1$  arcs). Full conditioning simply amounts to iterating single variable conditioning for each conditioned variable, thus it runs in time polynomial in the input size.  $\square$

**Proposition A.11.** ADD satisfies  $\text{CUT}_{\sim\gamma}$ ,  $\text{CUT}_{\preceq\gamma}$ , and  $\text{CUT}_{\succeq\gamma}$ .

*Proof.* This is trivial, because the values taken by a function are listed as the leaves of its ADD representation. Hence,  $\text{CUT}_{\sim\gamma}$  (resp.  $\text{CUT}_{\preceq\gamma}$ ,  $\text{CUT}_{\succeq\gamma}$ ) is the set of assignments the paths of which lead to a leaf with value  $\varphi = \gamma$  (resp.  $\varphi \leq \gamma$ ,  $\varphi \geq \gamma$ ). Replacing the label of every such leaf by  $a$  and that of all other leaves by  $b$ , which can be done in time linear in the size of the formula, we obtain an ADD representation of the function  $g$  defined by  $g(\vec{x}) = a$  if  $\vec{x}$  is in the cut and  $g(\vec{x}) = b$  otherwise.  $\square$

**Corollary A.12.** ADD satisfies

- $\text{CO}_{\sim\gamma}$ ,  $\text{CO}_{\preceq\gamma}$ , and  $\text{CO}_{\succeq\gamma}$ ;
- $\text{VA}_{\sim\gamma}$ ,  $\text{VA}_{\preceq\gamma}$ , and  $\text{VA}_{\succeq\gamma}$ ;
- $\text{ME}_{\sim\gamma}$ ,  $\text{ME}_{\preceq\gamma}$ , and  $\text{ME}_{\succeq\gamma}$ ;
- $\text{MX}_{\sim\gamma}$ ,  $\text{MX}_{\preceq\gamma}$ , and  $\text{MX}_{\succeq\gamma}$ ;
- $\text{CT}_{\sim\gamma}$ ,  $\text{CT}_{\preceq\gamma}$ , and  $\text{CT}_{\succeq\gamma}$ .

*Proof.* This comes directly from the previous result: ADD satisfies  $\text{CUT}_{\sim\gamma}$  (resp.  $\text{CUT}_{\preceq\gamma}$ ; resp.  $\text{CUT}_{\succeq\gamma}$ ), so an ADD formula  $\alpha$  can be turned in polynomial time into an ADD formula  $\beta$  such that  $f_\beta(\vec{x}) = 1$  if  $\vec{x}$  is in  $\text{CUT}_{\sim\gamma}(f_\alpha)$  (resp.  $\text{CUT}_{\preceq\gamma}(f_\alpha)$ ; resp.  $\text{CUT}_{\succeq\gamma}(f_\alpha)$ ) and 0 otherwise (we simply take  $a = 1$  and  $b = 0$ ). Thanks to Proposition A.8,  $\beta$  can be turned in polynomial time into an equivalent MDD formula. Since MDD satisfies the consistency, validity, model enumeration, model extraction, and model counting queries (Amilhastre et al. 2012), this proves that ADD satisfies  $\text{CO}_{\sim\gamma}$ ,  $\text{VA}_{\sim\gamma}$ ,  $\text{ME}_{\sim\gamma}$ ,  $\text{MX}_{\sim\gamma}$ , and  $\text{CT}_{\sim\gamma}$  (resp.  $\text{CO}_{\preceq\gamma}$ ,  $\text{VA}_{\preceq\gamma}$ ,  $\text{ME}_{\preceq\gamma}$ ,  $\text{MX}_{\preceq\gamma}$ , and  $\text{CT}_{\preceq\gamma}$ ; resp.  $\text{CO}_{\succeq\gamma}$ ,  $\text{VA}_{\succeq\gamma}$ ,  $\text{ME}_{\succeq\gamma}$ ,  $\text{MX}_{\succeq\gamma}$ , and  $\text{CT}_{\succeq\gamma}$ ).  $\square$

**Proposition A.13.** ADD, SLDD<sub>+</sub>, SLDD<sub>×</sub>, and AADD satisfy  $\text{OPT}_{\max}$  and  $\text{OPT}_{\min}$ .

*Proof.* The satisfaction of  $\text{OPT}_{\max}$  and  $\text{OPT}_{\min}$  by AADD is due to Sanner and McAllester (2005); they showed that the maximal (resp. minimal) value reached by  $f_\alpha^{\text{AADD}}$  is  $q_0 + f_0$

(resp.  $q_0$ ), where  $\langle q_0, f_0 \rangle$  is the offset of  $\alpha$  (assumed to be normalized). Then the satisfaction of  $\text{OPT}_{\max}$  and  $\text{OPT}_{\min}$  by  $\text{ADD}$ ,<sup>4</sup>  $\text{SLDD}_+$ , and  $\text{SLDD}_\times$ <sup>5</sup> is a consequence of Proposition A.7.  $\square$

**Proposition A.14.** *ADD,  $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and AADD satisfy  $\text{CUT}_{\max}$  and  $\text{CUT}_{\min}$ .*

*Proof.* Let us recall that, for any normalized AADD representation  $\alpha$ , the assignments  $\vec{x}$  such that  $f_\alpha^{\text{AADD}}(\vec{x})$  is maximal correspond to the paths following only the arcs  $e$  such that  $q_e + f_e = 1$  (they always exist when  $\alpha$  is normalized). It is then possible to get an ADD representation of  $\text{CUT}_{\max}^{\text{AADD}}(f_\alpha^{\text{AADD}})$  by replacing the leaf with a new one labeled with  $a$  (the  $a$ -node) and creating a second leaf labeled with  $b$  (the  $b$ -node): the arcs such that  $q_e + f_e \neq 1$  are then redirected to the  $b$ -node (this is done in time linear in the number of arcs). The structure is cleaned by recursively deleting nodes without incoming arcs; this cleaning process is also in linear time. The procedure is thus a polynomial algorithm transforming an AADD formula  $\alpha$  into an ADD representation of  $\text{CUT}_{\max}^{\text{AADD}}(f_\alpha^{\text{AADD}})$ . This algorithm allow us to conclude about  $\text{CUT}_{\max}$  for all four languages, thanks to Proposition A.7: since  $\text{ADD} \geq_\ell \text{AADD}$ , AADD satisfies  $\text{CUT}_{\max}$  (we can transform the resulting ADD formula into AADD) and ADD too (we can transform the initial formula into AADD, and keep the resulting ADD formula);  $\text{SLDD}_+$  (resp.  $\text{SLDD}_\times$ ) satisfies  $\text{CUT}_{\max}$  because  $\text{SLDD}_+ \geq_\ell \text{AADD}$  (resp.  $\text{SLDD}_\times \geq_\ell \text{AADD}$ ), which allows us to “prepare” the formula to use the algorithm, and  $\text{ADD} \geq_\ell \text{SLDD}_+$  (resp.  $\text{ADD} \geq_\ell \text{SLDD}_\times$ ), which allows us to translate the result into the targeted language.

The proof is similar for  $\text{CUT}_{\min}$ , since for any normalized AADD representation  $\alpha$ , the assignments  $\vec{x}$  such that  $f_\alpha^{\text{AADD}}(\vec{x})$  is minimal correspond to the paths following only the arcs  $e$  such that  $q_e = 0$  (they always exist when  $\alpha$  is normalized). The leaf becomes the  $a$ -node and the arcs  $e$  such that  $q_e \neq 0$  are redirected to the  $b$ -node.  $\square$

**Corollary A.15.** *ADD,  $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and AADD satisfy  $\text{ME}_{\max}$ ,  $\text{ME}_{\min}$ ,  $\text{MX}_{\max}$ ,  $\text{MX}_{\min}$ ,  $\text{CT}_{\max}$ ,  $\text{CT}_{\min}$ .*

*Proof.* This comes directly from the previous result: for any  $L \in \{\text{ADD}, \text{SLDD}_\times, \text{SLDD}_+, \text{AADD}\}$ ,  $L$  satisfies  $\text{CUT}_{\max}$  (resp.  $\text{CUT}_{\min}$ ), so an  $L$  formula can be turned in polynomial time into an  $L$  formula  $\alpha$  such that  $f_\alpha^L(\vec{x}) = 1$  if  $\vec{x}$  is a maximal (resp. minimal) value of  $f_\alpha^L$ , and 0 otherwise (we simply take  $a = 1$  and  $b = 0$ ). Thanks to Proposition A.8,  $\alpha$  can be turned in polynomial time into an equivalent MDD formula. Since MDD satisfies the model enumeration, model counting, and model extraction queries, this

<sup>4</sup>The satisfaction of  $\text{OPT}_{\max}$  and  $\text{OPT}_{\min}$  by  $\text{ADD}$  is obvious (just explore the terminal nodes to get the maximal or minimal value labeling one of them).

<sup>5</sup>For  $\text{SLDD}_+$  and  $\text{SLDD}_\times$ , our result also coheres with that of Wilson (2005), despite the fact that  $(\mathbb{R}^+, \min, +)$  and  $(\mathbb{R}^+, \max, +)$  are not semirings. Indeed, this does not matter, the important point being the addition-is-max (or addition-is-min) assumption. See the original paper from Wilson (2005) for details.

proves that  $L$  satisfies  $\text{ME}_{\max}$ ,  $\text{CT}_{\max}$ , and  $\text{MX}_{\max}$  (resp.  $\text{ME}_{\min}$ ,  $\text{CT}_{\min}$ , and  $\text{MX}_{\min}$ ).  $\square$

**Proposition A.16.**  *$\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and AADD satisfy  $\text{CO}_{\geq\gamma}$ ,  $\text{VA}_{\geq\gamma}$ ,  $\text{CO}_{\leq\gamma}$ ,  $\text{VA}_{\leq\gamma}$ .*

*Proof.* • To determine whether there exists an  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \geq \gamma$  (resp.  $f_\alpha^L(\vec{x}) \leq \gamma$ ), it is sufficient to compute the maximal (resp. minimal) value  $v^*$  (resp.  $v_*$ ) reached by  $f_\alpha^L$ ; this can be done in polynomial time since  $\text{OPT}_{\max}$  (resp.  $\text{OPT}_{\min}$ ) is satisfied by all three languages (Proposition A.13). Then it is enough to compare this value to  $\gamma$ , since  $\exists \vec{x}, f_\alpha^L(\vec{x}) \geq \gamma \iff v^* \geq \gamma$  (resp.  $\exists \vec{x}, f_\alpha^L(\vec{x}) \leq \gamma \iff v_* \leq \gamma$ ).

• To determine whether all the  $\vec{x}$  are such that  $f_\alpha^L(\vec{x}) \geq \gamma$  (resp.  $f_\alpha^L(\vec{x}) \leq \gamma$ ), it is sufficient to compute the minimal (resp. maximal) value  $v_*$  (resp.  $v^*$ ) reached by  $f_\alpha^L$ ; this can be done in polynomial time since  $\text{OPT}_{\min}$  (resp.  $\text{OPT}_{\max}$ ) is satisfied by all three languages. Then it is enough to compare this value to  $\gamma$ , since it holds that  $\forall \vec{x}, f_\alpha^L(\vec{x}) \geq \gamma \iff v_* \geq \gamma$  (resp.  $\forall \vec{x}, f_\alpha^L(\vec{x}) \leq \gamma \iff v^* \leq \gamma$ ).  $\square$

**Corollary A.17.**  *$\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and AADD satisfy  $\text{VA}_{\sim\gamma}$ .*

*Proof.* Checking whether all the  $\vec{x}$  are such that  $f_\alpha^L(\vec{x}) = \gamma$  is equivalent to checking whether for each  $\vec{x}$ , both  $f_\alpha^L(\vec{x}) \geq \gamma$  and  $f_\alpha^L(\vec{x}) \leq \gamma$  hold. Since all three languages satisfy both  $\text{VA}_{\geq\gamma}$  and  $\text{VA}_{\leq\gamma}$  (Proposition A.16), they also satisfy  $\text{VA}_{\sim\gamma}$ . Note that this proof does not work for  $\text{CO}_{\sim\gamma}$ .  $\square$

**Proposition A.18.**  *$\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and AADD do not satisfy  $\text{CO}_{\sim\gamma}$ , unless  $P = NP$ .*

*Proof.* We first show that  $\text{SLDD}_+$  does not satisfy  $\text{CO}_{\sim\gamma}$  unless  $P = NP$ . This follows from Hadzic and Andersen (2006, Theorem 3), by reduction of  $\text{SUBSET SUM}$  (given a set of integers  $E = \{i_1, \dots, i_n\}$  of cardinality  $n$  and an integer  $k$ , is there a subset of  $E$  summing to  $k$ ?) which is NP-complete (Garey and Johnson 1979) even if all the integers in  $E$  are positive.

Let us associate with  $E$  in polynomial time an  $\text{SLDD}_+$  formula  $\alpha_E$  over a set  $\mathcal{X} = \{x_1, \dots, x_n\}$  of  $n$  Boolean variables as follows (see Figure 1): the root of  $\alpha_E$  is labeled with  $x_1$ ,  $\alpha_E$  contains  $n$  internal nodes respectively labeled with  $x_1, \dots, x_n$  plus one leaf; for each  $i_j$  of  $E$ , we build two arcs  $e_j$  and  $e'_j$  in  $\alpha_E$  from the  $x_j$  node to the  $x_{j+1}$  node (or to the leaf when  $j = n$ ):  $e_j$  corresponds to  $x_j = 0$  and  $\varphi(e_j) = 0$ ;  $e'_j$  corresponds to  $x_j = 1$  and  $\varphi(e'_j) = i_j$ . This construction is done in time linear in  $|E|$ . Each  $\vec{x}$  (and thus each path in  $\alpha$ ) is in bijection with a subset of  $E$  and  $f_\alpha^{\text{SLDD}_+}(\vec{x})$  is equal to the sum of the elements in this subset.

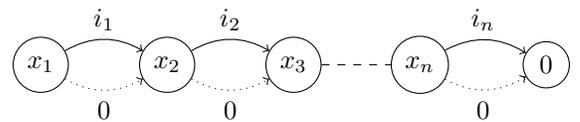


Figure 1: The  $\text{SLDD}_+$  formula  $\alpha_E$

If  $\text{SLDD}_+$  satisfied  $\text{CO}_{\sim\gamma}$ , then it would be possible to check in polynomial time whether there exists a subset of  $E$  summing to  $k$  – i.e., to solve the SUBSET SUM problem. Hence  $\text{SLDD}_+$  does not satisfy  $\text{CO}_{\sim\gamma}$ , unless  $\text{P} = \text{NP}$ .

Now, a similar construction holds for the  $\text{SLDD}_\times$  language, by considering the SUBSET PRODUCT problem (given a set of integers  $E = \{i_1, \dots, i_n\}$  of cardinality  $n$  and an integer  $k$ , is there a subset whose product is  $k$ ?) which is NP-complete as well (Garey and Johnson 1979).

Finally, if AADD satisfied  $\text{CO}_{\sim\gamma}$ , then  $\text{SLDD}_+$  also would, since any  $\text{SLDD}_+$  formula can be turned into an equivalent AADD formula in linear time (Proposition A.7). However, this is not the case unless  $\text{P} = \text{NP}$ , hence AADD does not satisfy  $\text{CO}_{\sim\gamma}$  unless  $\text{P} = \text{NP}$ .  $\square$

**Proposition A.19.** *Let  $L$  be a representation language over  $\mathcal{X}$  w.r.t.  $\mathcal{V}$ , where  $\mathcal{V}$  is totally ordered by a relation  $\succeq$ . If  $L$  satisfies  $\text{MX}_{\sim\gamma}$  or  $\text{CT}_{\sim\gamma}$ , then  $L$  satisfies  $\text{CO}_{\sim\gamma}$ .*

*Proof.* If  $L$  satisfies  $\text{MX}_{\sim\gamma}$ , then we can find in polynomial time an  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \sim \gamma$  or the information that there is no such assignment, so we can check in polynomial time whether there exists an  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \sim \gamma$ . This shows that  $\text{MX}_{\sim\gamma}$  implies  $\text{CO}_{\sim\gamma}$ . Now, it is obvious that counting the number of  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \sim \gamma$  indicates whether there exists an  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \sim \gamma$  (the answer is yes if and only if this number is positive); hence if  $L$  satisfies  $\text{CT}_{\sim\gamma}$ , it also satisfies  $\text{CO}_{\sim\gamma}$ .  $\square$

**Proposition A.20.** *Let  $L$  be a representation language over  $\mathcal{X}$  w.r.t.  $\mathcal{V}$ , where  $\mathcal{V}$  is totally ordered by a relation  $\succeq$ . If  $L$  satisfies  $\text{ME}_{\sim\gamma}$ , then  $L$  satisfies  $\text{CO}_{\sim\gamma}$ .*

*Proof.* If  $L$  satisfies  $\text{ME}_{\sim\gamma}$ , then there exists a polynomial  $p(\cdot, \cdot)$  and an algorithm  $A$  taking as input an  $L$  formula  $\alpha$  and listing every element of  $\text{CUT}^{\sim\gamma}(f_\alpha^L)$  in time bounded by  $p(|\alpha|, n)$ , where  $n$  is the number of elements in  $\text{CUT}^{\sim\gamma}(f_\alpha^L)$ .

Now, let  $A'$  be the algorithm that, taking as input an  $L$  formula  $\alpha$ , simulates the execution of  $A$  on input  $\alpha$ , and stops after at most  $p(|\alpha|, 0)$  steps. The result it returns depends on what happened during the simulation. There are three possible cases:

- (i) if  $A$  stopped without returning anything, then  $A'$  returns 0;
- (ii) if  $A$  stopped after having returned at least one assignment, then  $A'$  returns 1;
- (iii) if  $A$  did not stop by itself, then  $A'$  returns 1.

Obviously  $A'$  runs in time polynomial in the size of  $\alpha$ . Now, it is not hard to see that  $A'$  returns 1 if there exists an assignment  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \sim \gamma$ , and 0 otherwise.

Indeed, suppose that no such assignment exists. Then by definition,  $\text{CUT}^{\sim\gamma}(f_\alpha^L)$  is empty, so  $A$  stops without having returned anything after at most  $p(|\alpha|, 0)$  steps. This is case i:  $A'$  returns 0.

Now, suppose that there exists an assignment  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \sim \gamma$ . This means that  $\text{CUT}^{\sim\gamma}(f_\alpha^L)$  is not empty; let

us denote  $n$  its cardinal. The algorithm  $A$  returns  $n$  assignments and stops after at most  $p(|\alpha|, n)$  steps. Nothing prevents  $p(|\alpha|, n)$  to be lower than  $p(|\alpha|, 0)$ : this corresponds to case ii, in which  $A'$  returns 1. Finally, in the (probably less unusual) case when  $p(|\alpha|, n) > p(|\alpha|, 0)$ ,  $A'$  has to interrupt the simulation of  $A$ ; this is case iii, in which  $A'$  returns 1.

In conclusion, if  $L$  satisfies  $\text{ME}_{\sim\gamma}$ , then there exists a polynomial-time algorithm deciding whether there exists an assignment  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \sim \gamma$ , that is,  $L$  satisfies  $\text{CO}_{\sim\gamma}$ .  $\square$

**Proposition A.21.**  *$\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and AADD do not satisfy  $\text{MX}_{\sim\gamma}$ ,  $\text{CT}_{\sim\gamma}$ , or  $\text{ME}_{\sim\gamma}$ , unless  $\text{P} = \text{NP}$ .*

*Proof.* From Proposition A.18, none of these languages satisfies  $\text{CO}_{\sim\gamma}$  unless  $\text{P} = \text{NP}$ . This implies, from Propositions A.19 and A.20, that they cannot satisfy  $\text{MX}_{\sim\gamma}$ ,  $\text{CT}_{\sim\gamma}$ , or  $\text{ME}_{\sim\gamma}$ , unless  $\text{P} = \text{NP}$ .  $\square$

**Proposition A.22.**  *$\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and AADD satisfy  $\text{MX}_{\succeq\gamma}$  and  $\text{MX}_{\preceq\gamma}$ .*

*Proof.* To get an assignment  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \geq \gamma$ , simply check whether  $\gamma$  is strictly greater than the maximal value  $v^*$ , which can be obtained in polynomial time since all the languages satisfy  $\text{OPT}_{\max}$  (Proposition A.13). If so, we know that there is no assignment  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \geq \gamma$ . In the remaining case (i.e., when  $v^* \geq \gamma$ ), the optimal assignments  $\vec{x}^*$  are such that  $f_\alpha^L(\vec{x}^*) = v^* \geq \gamma$ ; since all three languages satisfy  $\text{MX}_{\max}$  (Proposition A.15), one can get such an  $\vec{x}^*$  in polynomial time. Since by construction  $\vec{x}^* \in \text{CUT}^{\succeq\gamma}$ , this proves that all three languages satisfy  $\text{MX}_{\succeq\gamma}$ . We prove in the same way that they satisfy  $\text{MX}_{\preceq\gamma}$ , as a consequence of the satisfaction of  $\text{OPT}_{\min}$  (Proposition A.13) and  $\text{MX}_{\min}$  (Proposition A.15).  $\square$

**Proposition A.23.**  *$\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and AADD satisfy  $\text{ME}_{\succeq\gamma}$  and  $\text{ME}_{\preceq\gamma}$ .*

*Proof.* We prove the result for AADD first, using the basic idea that at any node  $N$  of a normalized AADD formula, there is at least one path to the leaf of valuation 1 and one path to the leaf of valuation 0. A path reaching  $N$  provides an offset, gathered from its arcs, say  $\langle p, q \rangle$ , so the cheapest path traversing  $N$  has valuation  $p$  and the most expensive one has valuation  $p + q$ .

This allows us to traverse the graph in reverse topological order, only developing a node if it yields at least one element of the cut (e.g., if we want the elements of  $\text{CUT}^{\succeq\gamma}$ , node  $N$  is only developed if  $p + q \geq \gamma$ ). We thus get the entire list of elements in the cut by a tree search algorithm that is backtrack-free, and thus polynomial in the number of leaves reached, which is exactly the number of assignments  $\vec{x}$  such that  $f_\alpha^L(\vec{x}) \geq \gamma$  (resp.  $f_\alpha^L(\vec{x}) \leq \gamma$ ). A recursive implementation of this procedure is detailed in Algorithm 3 (the top call is supposed to pass  $\vec{x} = \vec{\emptyset}$ ). Note that the assignments listed are not always complete, in the sense that they only feature variables encountered on the corresponding path. It is straightforward to extend each resulting partial assignment  $\vec{z}$  into the full list of complete assignments of which  $\vec{z}$  is a restriction.

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**Algorithm 3:** EnumModelsAADD( $\alpha, \vec{x}, \mathcal{R}, \gamma$ )

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input : an AADD formula  $\alpha$ , of root  $N$ , with offset  $\langle q_0, f_0 \rangle$ ; a current assignment  $\vec{x}$ ; a relation  $\mathcal{R} \in \{\leq, \geq\}$ , and a threshold  $\gamma \in \mathbb{R}^+$   
output: the list of assignments  $\vec{x} \cdot \vec{y}$  such that  $f_\alpha^{\text{AADD}}(\vec{x} \cdot \vec{y}) \mathcal{R} \gamma$

- 1 if  $\mathcal{R} = \leq$  then
- 2   | if  $q_0 > \gamma$  then
- 3   |   | return
- 4 else
- 5   | if  $q_0 + f_0 < \gamma$  then
- 6   |   | return
- 7 if  $N$  is the leaf then
- 8   | print  $\vec{x}$
- 9   | return
- 10 let  $y := \text{Var}(N)$
- 11 foreach arc  $a$  going out of  $N$  do
- 12   | let  $\vec{y} = \langle y, v(a) \rangle$
- 13   | let  $M$  be the destination node of  $a$
- 14   | let  $\alpha'$  be the AADD formula rooted at the destination node of  $a$ , with offset  $\langle q_0 + f_0 \times q_a, f_0 \times f_a \rangle$
- 15   | EnumModelsAADD( $\alpha, \vec{x} \cdot \vec{y}$ ),  $\mathcal{R}, \gamma$ )

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This proves that AADD satisfies  $\text{ME}_{\geq \gamma}$  and  $\text{ME}_{\leq \gamma}$ ; we can deduce from Proposition A.7 that SLDD<sub>+</sub> and SLDD<sub>×</sub> also satisfy these two queries.  $\square$

**Lemma A.24.** Let  $\otimes \in \{+, \times\}$ . There exists a polynomial-time algorithm mapping any SLDD <sub>$\otimes$</sub>  formula  $\alpha$  and every set of variables  $X \supseteq \text{Var}(\alpha)$  to an equivalent SLDD <sub>$\otimes$</sub>  formula in which each path mentions all the variables in  $X$ .

*Proof.* Let  $\alpha$  be an SLDD <sub>$\otimes$</sub>  formula, in which variables are ordered with respect to  $\triangleleft$ . Let  $X = \{x_1, \dots, x_n\}$  be a set of variables containing all variables in  $\text{Var}(\alpha)$ ; without loss of generality, let us assume that  $x_1 \triangleleft \dots \triangleleft x_n$ .

What we want is to build an L formula  $\alpha'$  in which every path from the root to the leaf is of the form  $\langle a_1, \dots, a_n \rangle$  where each  $a_i$  (with  $i \in \{1, \dots, n-1\}$ ) is an arc from a node labeled with  $x_i$  to a node labeled with  $x_{i+1}$ , and  $a_n$  is an arc from a node labeled with  $x_n$  to the leaf node.

This property can be easily guaranteed in polynomial time: for each arc  $a$  of  $\alpha$  from a node  $N_i$  labeled with  $x_i$  ( $i \in \{1, \dots, n-1\}$ ) to a node  $M$  such that either  $M$  is the leaf or  $\text{Var}(M) = x_{i+j}$  with  $j > 1$ ,

1. add  $j-1$  new nodes  $N_{i+1}, \dots, N_{i+j-1}$  respectively labeled  $x_{i+1}, \dots, x_{i+j-1}$ ;
2. redirect  $a$  to  $N_{i+1}$ ; and
3. for each of the new nodes  $N_k$  ( $k \in \{i, \dots, i+j-1\}$ ), for each value  $d \in D_{x_k}$ , add an arc  $a_{k,d}$  from  $N_k$  to  $N_{k+1}$  (conveniently considering  $N_{i+j}$  to be  $M$ ) with  $v(a_{k,d}) = d$  and  $\varphi(a_{k,d})$  is the neutral element of  $\otimes$  (0 for  $+$  and 1 for  $\times$ ).  $\square$

**Lemma A.25.** There exists a polynomial algorithm mapping any SLDD<sub>+</sub> formula  $\alpha$  of maximal value  $v^*$  and any constant  $K \in \mathbb{R}^+$  such that  $K \geq v^*$ , to an SLDD<sub>+</sub> representation of the function  $g = K - f_\alpha^{\text{SLDD}^+}$ .

*Proof.* This is not hard to prove, but care must be taken to always remain in the fragment (negative arc valuations are forbidden by the definitions).

We first compute  $v^*$ , the maximal value taken by  $f_\alpha^{\text{SLDD}^+}$ ; this can be done in polynomial time, since SLDD<sub>+</sub> satisfies  $\text{OPT}_{\max}$  (Proposition A.13). Then we modify  $\alpha$  so that all of its paths mentions all the variables in  $\text{Var}(\alpha)$  (Lemma A.24 states that it can be done in polynomial time), and every arc label  $\varphi$  (including the offset) is replaced by  $K - \varphi$ , which is guaranteed to be in  $\mathbb{R}^+$  since  $K \geq v^*$ . We call  $\alpha'$  the modified formula.

It should be clear that for any  $\vec{x}$ ,  $f_{\alpha'}^{\text{SLDD}^+}(\vec{x}) = K \times (1 + |\text{Var}(\alpha)|) - f_\alpha(\vec{x})$ , since along each path, we added  $K$  as many times as there are arcs, plus one time for the offset. Applying the normalization procedure on  $\alpha'$  (in linear time, thanks to Proposition A.5), we get an SLDD<sub>+</sub> formula  $\alpha''$  with offset  $K \times (1 + |\text{Var}(\alpha)|) - v^*$ : indeed, the offset of a normalized SLDD<sub>+</sub> formula is the minimal value taken by the function it represents (each node has at least one 0-valued outgoing arc).

We now simply have to subtract  $K \times |\text{Var}(\alpha)|$  to the offset of  $\alpha''$  to obtain an SLDD<sub>+</sub> representation of the function  $g = K - f_\alpha^{\text{SLDD}^+}$ .  $\square$

**Proposition A.26.** SLDD<sub>+</sub>, SLDD<sub>×</sub>, and AADD do not satisfy  $\text{CT}_{\geq \gamma}$  or  $\text{CT}_{\leq \gamma}$  unless  $\text{P} = \text{NP}$ .

*Proof.* We first show that SLDD<sub>+</sub> cannot satisfy  $\text{CT}_{\geq \gamma}$  unless  $\text{P} = \text{NP}$ . Let us suppose it is the case; then we can compute in polynomial time the number  $M_{\geq \gamma}$  of assignments  $\vec{x}$  such that  $f_\alpha^{\text{SLDD}^+}(\vec{x}) \geq \gamma$  (i.e., compute  $|\text{CUT}_{\geq \gamma}(f_\alpha^{\text{SLDD}^+})|$ ). It is moreover always possible to compute the maximal value  $v^*$  reached by  $f_\alpha^{\text{SLDD}^+}$  (SLDD<sub>+</sub> satisfies  $\text{OPT}_{\max}$ , Proposition A.13) and to get a representation in the same language of the function  $g_\alpha = v^* - f_\alpha^{\text{SLDD}^+}$  (thanks to Lemma A.25). Since we suppose that  $\text{CT}_{\geq \gamma}$  is satisfied, then it is possible to compute in polynomial time the number  $M$  of assignments  $\vec{x}$  such that  $v^* - f_\alpha^{\text{SLDD}^+}(\vec{x}) \geq v^* - \gamma$ , taking  $v^* - \gamma$  as the threshold. We can then compute the number  $M_{> \gamma} = |D_1 \times \dots \times D_n| - M$  of assignments  $\vec{x}$  such that  $v^* - f_\alpha^{\text{SLDD}^+}(\vec{x}) < v^* - \gamma$ : by construction,  $M_{> \gamma}$  is equal to the number of assignments  $\vec{x}$  such that  $f_\alpha^{\text{SLDD}^+}(\vec{x}) > \gamma$ .

Since we know that there are  $M_{\geq \gamma}$  assignments  $\vec{x}$  such that  $f_\alpha^{\text{SLDD}^+}(\vec{x}) \geq \gamma$ , we can deduce that there are precisely  $M_{> \gamma} - M_{\geq \gamma}$  assignments  $\vec{x}$  such that  $f_\alpha^{\text{SLDD}^+}(\vec{x}) = \gamma$ .

We have proved that the satisfaction of  $\text{CT}_{\geq \gamma}$  implies that of  $\text{CT}_{\sim \gamma}$ ; since SLDD<sub>+</sub> does not satisfy  $\text{CT}_{\sim \gamma}$  unless  $\text{P} = \text{NP}$  (Proposition A.21), we conclude that SLDD<sub>+</sub> does not satisfy  $\text{CT}_{\geq \gamma}$  unless  $\text{P} = \text{NP}$ .

We prove that SLDD<sub>+</sub> does not satisfy  $\text{CT}_{\leq \gamma}$  unless  $\text{P} = \text{NP}$  in a similar way: the satisfaction of  $\text{CT}_{\leq \gamma}$  would allow us to compute  $M_{\leq \gamma}$  and  $M_{< \gamma}$  using the same technique, hence it would imply the satisfaction of  $\text{CT}_{\sim \gamma}$ .

These two results imply that  $\text{SLDD}_\times$  cannot satisfy  $\text{CT}_{\leq\gamma}$  or  $\text{CT}_{\geq\gamma}$  unless  $\text{P} = \text{NP}$ . Indeed, an  $\text{SLDD}_+$  representation of a function  $g$  can be transformed in polynomial time into an  $\text{SLDD}_\times$  representation of the function  $h = 2^g$ , simply by replacing the label of the leaf by 1 and the value  $\varphi$  of each arc (including the offset) by  $2^\varphi$ . If  $\text{SLDD}_\times$  satisfied  $\text{CT}_{\leq\gamma}$  (resp.  $\text{CT}_{\geq\gamma}$ ), we could obtain in polynomial time the number of assignments  $\vec{x}$  such that  $2^{g(\vec{x})} \leq 2^\gamma$  (resp.  $2^{g(\vec{x})} \geq 2^\gamma$ ), which is the same number as  $\text{CUT}^{\leq\gamma}(g)$  (resp.  $\text{CUT}^{\geq\gamma}(g)$ ). Thus the satisfaction of  $\text{CT}_{\leq\gamma}$  (resp.  $\text{CT}_{\geq\gamma}$ ) on  $\text{SLDD}_\times$  would imply its satisfaction on  $\text{SLDD}_+$ , yet we have just proved that it is not satisfied by  $\text{SLDD}_+$  unless  $\text{P} = \text{NP}$ .

Finally,  $\text{AADD}$  does not satisfy  $\text{CT}_{\geq\gamma}$  (resp.  $\text{CT}_{\leq\gamma}$ ), because this would imply that  $\text{SLDD}_+$  also satisfies it, since  $\text{SLDD}_+ \geq_\ell \text{AADD}$  (Proposition A.7).  $\square$

**Proposition A.27.**  $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and  $\text{AADD}$  do not satisfy  $\text{CUT}_{\sim\gamma}$ .

*Proof.* We consider a set  $\mathcal{X} = \{y_1, \dots, y_n, z_1, \dots, z_n\}$  of  $2n$  Boolean variables, and  $\triangleleft$  the total order on  $\mathcal{X}$  defined by  $y_1 \triangleleft \dots \triangleleft y_n \triangleleft z_1 \triangleleft \dots \triangleleft z_n$ . Let  $f^{\text{sum}}$  and  $f^{\text{prod}}$  be the functions defined by  $f^{\text{sum}}(\vec{y} \cdot \vec{z}) = \sum_{i=1}^n y_i 2^{n-i} + \sum_{i=1}^n z_i 2^{n-i}$ , and  $f^{\text{prod}}(\vec{y} \cdot \vec{z}) = 2^{f^{\text{sum}}(\vec{y} \cdot \vec{z})}$ . Figure 2 shows how  $f^{\text{sum}}$  (resp.  $f^{\text{prod}}$ ) can be represented as an  $\text{SLDD}_+$  (resp.  $\text{SLDD}_\times$ ) formula ordered by  $\triangleleft$  containing only  $2n + 1$  nodes and  $2n$  arcs. Since  $\text{SLDD}_+ \geq_\ell \text{AADD}$  (Proposition A.7),  $f^{\text{sum}}$  also has a linear-sized  $\text{AADD}$  representation.

Let  $f^{\text{eq}}$  be the Boolean function defined by  $f^{\text{eq}}(\vec{y} \cdot \vec{z}) = 1$  if  $f^{\text{sum}}(\vec{y} \cdot \vec{z}) = 2^n$ , and 0 otherwise. Note that  $f^{\text{eq}}$  also equals 1 if and only if  $f^{\text{prod}}(\vec{y} \cdot \vec{z}) = 2^{2^n}$ .

We now show that there is no polynomial-sized representation of  $f^{\text{eq}}$  as an  $\text{OBDD}_{\triangleleft}$  formula. The proof basically relies on a result by Sieling and Wegener (1993), showing that for any Boolean function  $f$  over  $\mathcal{X}$ , if the number of restrictions on  $\{y_1, \dots, y_n\}$  which are distinct and depends on  $z_1$  is equal to  $m$ , then any  $\text{OBDD}_{\triangleleft}$  formula representing  $f$  contains at least  $m$  nodes labeled with  $z_1$ . We show that it is the case for  $f^{\text{eq}}$ .

Clearly enough, every assignment  $\vec{y}$  over  $\{y_1, \dots, y_n\}$  (resp.  $\vec{z}$  over  $\{z_1, \dots, z_n\}$ ) is associated in a bijective way with a natural number  $N(\vec{y}) = \sum_{i=1}^n y_i 2^{n-i}$  (resp.  $N(\vec{z}) = \sum_{i=1}^n z_i 2^{n-i}$ ) which belongs to  $[0, 2^n - 1]$ . In particular, there are  $2^n$  distinct assignments over  $\{y_1, \dots, y_n\}$ . Consider now two distinct assignments  $\vec{y}$  and  $\vec{y}'$  over  $\{y_1, \dots, y_n\}$ . The two restrictions  $f^{\text{eq}}_{\vec{y}}$  and  $f^{\text{eq}}_{\vec{y}'}$  are distinct, since they have distinct support sets (i.e., the set of assignments over  $\{z_1, \dots, z_n\}$  that makes the restriction equal to 1), namely  $\{\vec{z} \mid N(\vec{y}) + N(\vec{z}) = 2^n\}$  and  $\{\vec{z} \mid N(\vec{y}') + N(\vec{z}) = 2^n\}$ , which are singletons (there is exactly one  $\vec{z}$  for each  $\vec{y}$ ).

Furthermore, for each assignment  $\vec{y}$  over  $\{y_1, \dots, y_n\}$ ,  $f^{\text{eq}}_{\vec{y}}$  depends on  $z_1$ , i.e.,  $f^{\text{eq}}_{\vec{y} \cdot (z_1, 0)} \neq f^{\text{eq}}_{\vec{y} \cdot (z_1, 1)}$ : this is obvious since the support set of  $f^{\text{eq}}_{\vec{y}}$  is a singleton (one of the two functions always returns 0, whereas the other one returns 1 for exactly one assignment).

Consequently, according to the abovementioned result, any  $\text{OBDD}_{\triangleleft}$  formula representing  $f^{\text{eq}}$  contains at least  $2^n - 1$  nodes labeled with  $z_1$ : there can be no polynomial-sized  $\text{OBDD}_{\triangleleft}$  representation of  $f^{\text{eq}}$ .

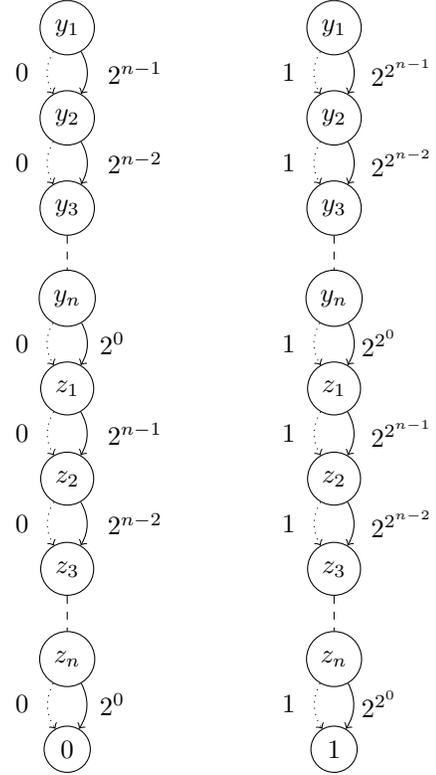


Figure 2: Left: An  $\text{SLDD}_+$  representation of  $f^{\text{sum}}$ . Right: An  $\text{SLDD}_\times$  representation of  $f^{\text{prod}}$ .

Now, note that the model set of  $f^{\text{eq}}$  is exactly  $\text{CUT}_{\sim\gamma}(f^{\text{sum}}) = \text{CUT}_{\sim\gamma}(f^{\text{prod}})$  with  $\gamma = 2^n$ . This means that if  $\text{SLDD}_+$  (resp.  $\text{SLDD}_\times$ ,  $\text{AADD}$ ) satisfied  $\text{CUT}_{\sim\gamma}$ , we could get in time polynomial in  $n$  an  $\text{SLDD}_+$  (resp.  $\text{SLDD}_\times$ ,  $\text{AADD}$ ) representation of  $f^{\text{eq}}$  with  $a = 1$  and  $b = 0$ , that we could in turn translate in polynomial time into an  $\text{OBDD}_{\triangleleft}$  formula (using Proposition A.8, and remarking that an  $\text{MDD}$  over Boolean variables is an  $\text{OBDD}$ ). By contradiction,  $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and  $\text{AADD}$  do not satisfy  $\text{CUT}_{\sim\gamma}$ .  $\square$

**Proposition A.28.**  $\text{SLDD}_+$  does not satisfy  $\text{CUT}_{\geq\gamma}$  or  $\text{CUT}_{\leq\gamma}$ .

*Proof.* The first step is to show that  $\text{SLDD}_+$  satisfies  $\text{CUT}_{\geq\gamma}$  if and only if it satisfies  $\text{CUT}_{\leq\gamma}$ . Let  $a, b \in \mathbb{R}^+$  such that  $a > b$ , and let  $\gamma \in \mathbb{R}^+$ .

Suppose  $\text{SLDD}_+$  satisfies  $\text{CUT}_{\geq\gamma}$ . Computing the maximal value  $v^*$  of  $f_\alpha^{\text{SLDD}_+}$ , which can be done in polynomial time (Proposition A.13), and choosing a constant  $K \geq \max(v^*, \gamma)$ , Lemma A.25 states that we can build in polynomial time an  $\text{SLDD}_+$  representation  $\alpha'$  of the function  $K - f_\alpha^{\text{SLDD}_+}$ . Now, applying the  $\text{CUT}_{\geq\gamma}$  transformation to  $\alpha'$  with a threshold of  $K - \gamma$ , which by hypothesis can be

done in polynomial time, we get an  $\text{SLDD}_+$  representation of the function  $g$  defined by  $g(\vec{x}) = a$  if  $f_{\alpha'}^{\text{SLDD}_+}(\vec{x}) \geq K - \gamma$ , i.e., if  $f_{\alpha}^{\text{SLDD}_+}(\vec{x}) \leq \gamma$ , and  $g(\vec{x}) = b$  otherwise:  $\text{CUT}_{\leq \gamma}$  is satisfied. This proves that if  $\text{SLDD}_+$  satisfies  $\text{CUT}_{\geq \gamma}$ , it also satisfies  $\text{CUT}_{\leq \gamma}$ . The same reasoning can be used to show that if it satisfies  $\text{CUT}_{\leq \gamma}$ , it also satisfies  $\text{CUT}_{\geq \gamma}$ . Hence, the satisfaction of either of the two implies the satisfaction of both. We now show that it also implies the satisfaction of  $\text{CUT}_{\sim \gamma}$ .

Let  $\gamma \in \mathbb{R}^+$  and let  $\alpha$  be an  $\text{SLDD}_+$  formula. We apply the  $\text{CUT}_{\geq \gamma}$  and  $\text{CUT}_{\leq \gamma}$  transformations on  $\alpha$ , with  $a = 1$  and  $b = 0$ . The resulting  $\text{SLDD}_+$  formulae can be turned in polynomial time into ordered MDDs (Proposition A.8), and since MDD satisfies  $\wedge \text{BC}$  (Amilhatre et al. 2012), we can obtain in polynomial time an MDD the models of which are exactly the assignments that are in  $\text{CUT}_{\geq \gamma}(f_{\alpha}) \cap \text{CUT}_{\leq \gamma}(f_{\alpha})$ , i.e., an MDD representing  $\text{CUT}_{\sim \gamma}(f_{\alpha})$ . We can replace the 0-leaf by some  $b \in \mathbb{R}^+$  and the 1-leaf by some  $a \in \mathbb{R}^+$  such that  $a > b$ : we get an ADD representation of the function  $h$  defined by  $h(\vec{x}) = a$  if  $\vec{x} \in \text{CUT}_{\sim \gamma}(f_{\alpha})$  and  $h(\vec{x}) = b$  otherwise. Since  $\text{ADD} \geq_{\ell} \text{SLDD}_+$  (Proposition A.7), this means that  $\text{SLDD}_+$  satisfies  $\text{CUT}_{\sim \gamma}$ .

All in all, if  $\text{SLDD}_+$  satisfies  $\text{CUT}_{\geq \gamma}$  (resp.  $\text{CUT}_{\leq \gamma}$ ), it also satisfies  $\text{CUT}_{\leq \gamma}$  (resp.  $\text{CUT}_{\geq \gamma}$ ), which in turn implies that it satisfies  $\text{CUT}_{\sim \gamma}$ , yet we have shown that it does not (Proposition A.27). Hence  $\text{SLDD}_+$  does not satisfy  $\text{CUT}_{\geq \gamma}$  or  $\text{CUT}_{\leq \gamma}$ .  $\square$

**Corollary A.29.**  $\text{SLDD}_{\times}$  and  $\text{AADD}$  do not satisfy  $\text{CUT}_{\geq \gamma}$  or  $\text{CUT}_{\leq \gamma}$ .

*Proof.* We show that if  $\text{SLDD}_{\times}$  satisfied  $\text{CUT}_{\geq \gamma}$  (resp.  $\text{CUT}_{\leq \gamma}$ ),  $\text{SLDD}_+$  would also satisfy it. Indeed, an  $\text{SLDD}_+$  representation  $\alpha$  of a function  $f$  can be transformed in polynomial time into an  $\text{SLDD}_{\times}$  representation  $\alpha'$  of the function  $g = 2^f$ , simply by replacing the label of the leaf by 1 and the value  $\varphi$  of each arc (including the offset) by  $2^{\varphi}$ . Suppose  $\text{SLDD}_{\times}$  satisfies  $\text{CUT}_{\geq \gamma}$  (resp.  $\text{CUT}_{\leq \gamma}$ ): taking  $a = 1$  and  $b = 0$ , we could obtain in polynomial time an  $\text{SLDD}_{\times}$  representing  $\text{CUT}_{\geq \gamma}(f)$  (resp.  $\text{CUT}_{\leq \gamma}(f)$ ), by taking the cut of  $\alpha'$  w.r.t. the threshold  $2^{\gamma}$ . We can transform the result into an MDD, which only takes polynomial time (Proposition A.8), then replace the 0-leaf by some  $b \in \mathbb{R}^+$  and the 1-leaf by some  $a \in \mathbb{R}^+$  such that  $a > b$ : we get an ADD representation of the function  $h$  defined by  $h(\vec{x}) = a$  if  $\vec{x}$  is in  $\text{CUT}_{\geq \gamma}(f)$  (resp.  $\text{CUT}_{\leq \gamma}(f)$ ) and  $h(\vec{x}) = b$  otherwise. Since  $\text{ADD} \geq_{\ell} \text{SLDD}_+$  (Proposition A.7), this means that  $\text{SLDD}_+$  satisfies  $\text{CUT}_{\geq \gamma}$  (resp.  $\text{CUT}_{\leq \gamma}$ ), yet we have shown that it does not (Proposition A.28). Consequently,  $\text{SLDD}_{\times}$  cannot satisfy  $\text{CUT}_{\geq \gamma}$  (resp.  $\text{CUT}_{\leq \gamma}$ ).

Now, using a reasoning similar to that of the previous paragraph, we show that if  $\text{AADD}$  satisfied  $\text{CUT}_{\geq \gamma}$  (resp.  $\text{CUT}_{\leq \gamma}$ ),  $\text{SLDD}_+$  also would. Indeed, an  $\text{SLDD}_+$  formula can be turned in polynomial time into an  $\text{AADD}$  formula (Proposition A.7); then we could apply  $\text{CUT}_{\geq \gamma}$  (resp.  $\text{CUT}_{\leq \gamma}$ ) on values 0 and 1, so that the resulting  $\text{AADD}$  formula can be turned in polynomial time into an MDD (Proposition A.8); then the leaves of the MDD can be re-labeled

with any  $a, b \in \mathbb{R}^+$ , and the resulting ADD formula be turned in polynomial time into an  $\text{SLDD}_+$  (Proposition A.7).  $\square$

## Combinations

**Proposition A.30.**  $\text{ADD}$ ,  $\text{SLDD}_+$ ,  $\text{SLDD}_{\times}$ , and  $\text{AADD}$  do not satisfy  $+\text{C}$ ,  $\times \text{C}$ ,  $\max \text{C}$ , or  $\min \text{C}$ .

*Proof.* The proofs rely on results in the Boolean case: Wegener (1987) has shown that there exists a family of formulae  $\Sigma_n$  over  $\{x_1, \dots, x_n\}$  where each  $\Sigma_n$  has a number of prime implicates cubic in  $n$  (hence each  $\Sigma_n$  has a polynomial-sized CNF representation) but every OBDD representation of  $\Sigma_n$  has a size exponential in  $n$ . We use the  $\Sigma_n$  family in all the following proofs.

**ADD does not satisfy  $+\text{C}$ .** For each  $n$ , for each of the clauses  $\delta_i$  of  $\Sigma_n$ , let  $\alpha_i$  be an ADD formula of size linear in the size of  $\delta_i$  (hence linear in  $n$ ) representing a term equivalent to  $\neg \delta_i$ , i.e., such that for each assignment  $\vec{x}$  over  $\{x_1, \dots, x_n\}$ ,  $f_{\alpha_i}^{\text{ADD}}(\vec{x}) = 0$  if  $\vec{x}$  satisfies  $\delta_i$ ,  $f_{\alpha_i}^{\text{ADD}}(\vec{x}) = 1$  otherwise. Suppose that  $\text{ADD}$  satisfied  $+\text{C}$ . In this case, it would be possible to compute in time polynomial in  $n$  an ADD representation of the function that associates with each assignment  $\vec{x}$  the number of clauses of  $\Sigma_n$  violated by  $\vec{x}$ . Since  $\text{ADD}$  satisfies  $\text{CUT}_{\min}$  (Proposition A.14), we could then compute from it in polynomial time an ADD representation with  $a = 1$  and  $b = 0$  of the set of assignments  $\vec{x}$  such that  $f_{\Sigma_n}(\vec{x}) = 1$  if  $\vec{x}$  does not violate any clause,  $f_{\Sigma_n}(\vec{x}) = 0$  otherwise. By construction, this ADD representation is also an OBDD representation of  $\Sigma_n$ . This would contradict the fact that every OBDD representation of  $\Sigma_n$  has exponential size.

**ADD does not satisfy  $\max \text{C}$ .** Consider the ADD formulae  $\alpha_i$  as in the proof for the  $+\text{C}$  case. If  $\text{ADD}$  satisfied  $\max \text{C}$ , then it would be possible to compute in time polynomial in  $n$  an ADD representation of the function  $f_{\Sigma_n}$  that associates with each assignment  $\vec{x}$  the value  $\max_i f_{\alpha_i}^{\text{ADD}}(\vec{x})$ . By construction, this value is equal to 1 if  $\Sigma$  is violated by  $\vec{x}$  and is equal to 0 otherwise. Hence the ADD representation of the function  $f_{\Sigma_n}$  also is an OBDD representation of  $\neg \Sigma_n$ . An OBDD representation of  $\Sigma_n$  could be easily obtained from it by labeling the 0-leaf with 1 and the 1-leaf with 0. This would contradict the fact that every OBDD representation of  $\Sigma_n$  has exponential size.

**ADD does not satisfy  $\times \text{C}$ .** For each of the clauses  $\delta_i$  of  $\Sigma_n$ , let  $\alpha_i$  be an ADD representation of  $\delta_i$ . Each  $\alpha_i$  can be easily generated in time linear in the size of  $\delta_i$ , hence in time linear in  $n$ . If  $\text{ADD}$  satisfied  $\times \text{C}$ , it would be possible to compute in time polynomial in  $n$  an ADD representation of the function  $f_{\Sigma_n}$  that associates with each assignment  $\vec{x}$  the value  $\prod_i f_{\alpha_i}^{\text{ADD}}(\vec{x})$ . This value is equal to 1 if  $\Sigma_n$  is satisfied by  $\vec{x}$  and to 0 otherwise. Hence it is an OBDD representation of  $\Sigma_n$ , contradiction.

**ADD does not satisfy  $\min \text{C}$ .** For each of the clauses  $\delta_i$  of  $\Sigma_n$ , let  $\alpha_i$  be an ADD representation of  $\delta_i$ . Each  $\alpha_i$  can be easily generated in time linear in the size of  $\delta_i$ , hence in time linear in  $n$ . If  $\text{ADD}$  satisfied  $\min \text{C}$ , then it would be possible to compute in time polynomial in  $n$  an ADD representation of the function  $f_{\Sigma_n}$  that associates with each assignment  $\vec{x}$

the value  $\min_i f_{\alpha_i}^{\text{ADD}}(\vec{x})$ . This value is equal to 0 if  $\Sigma$  is violated by  $\vec{x}$  and is equal to 1 otherwise. Hence it is an OBDD representation of  $\Sigma_n$ , contradiction.

SLDD<sub>+</sub>, SLDD<sub>×</sub>, and AADD do not satisfy +C, ×C, maxC, or minC. Let  $L \in \{\text{SLDD}_+, \text{SLDD}_\times, \text{AADD}\}$ . Any ADD formula can be transformed in linear time into an equivalent L formula (Proposition A.7). Furthermore, L satisfies CUT<sub>min</sub> (Proposition A.14). Finally, for each transformation, each one of the functions  $f_{\Sigma_n}$  considered in the above proofs takes its values in  $\{0, 1\}$ . Hence, thanks to Proposition A.8, each of its L representations (as computed as in the above proofs) could be turned in polynomial time into an equivalent MDD representation (which would be an OBDD representation, since every variable is a Boolean one). Once again, this would contradict the fact that every OBDD representation of  $\Sigma_n$  has exponential size.  $\square$

**Proposition A.31.** SLDD<sub>+</sub>, SLDD<sub>×</sub>, and AADD do not satisfy maxBC or minBC.

*Proof.* We first show that the satisfaction of maxBC implies that of CUT<sub>≤γ</sub>.

Let  $\gamma \in \mathcal{V}$ ; we can easily generate in constant time an ADD representation of the constant function  $f_\gamma$  defined by  $\forall \vec{x}, f_\gamma(\vec{x}) = \gamma$ . Given Proposition A.7, we can also generate in constant time an SLDD<sub>+</sub> (resp. SLDD<sub>×</sub>, resp. AADD) representation of  $f_\gamma$ .

Suppose that  $L \in \{\text{SLDD}_+, \text{SLDD}_\times, \text{AADD}\}$  satisfied maxBC. Then for any L formula  $\alpha$ , it would be possible to build in time polynomial in the size of  $\alpha$  an L representation  $\beta$  of the function  $g = \max(f_\alpha, f_\gamma)$ . Because each of the three languages satisfies OPT<sub>min</sub> (Proposition A.13), we could compute in polynomial time the minimum value  $v_*$  reached by  $g$ . If  $v_* > \gamma$ , then  $\forall \vec{x}, f_\alpha(\vec{x}) > \gamma$ . Accordingly,  $\text{CUT}^{\geq \gamma}(f_\alpha) = \emptyset$ . So the cut  $f_{\leq \gamma}$  on  $\{b, c\}$  is the constant function such that  $\forall \vec{x}, f_{\leq \gamma}(\vec{x}) = c$ , which can be represented in constant time as an L formula with only one node.

The case when  $v_* \leq \gamma$  is more complicated, but we show that it is possible to build in polynomial time an L representation of  $f_{\leq \gamma}$ , by redirecting the arcs not taking part in valuations equal to  $\gamma$ .

First, we can compute in polynomial time, for any arc  $a$  of an L formula  $\alpha$ , the cost  $\text{mincost}(a)$  of the cheapest assignment  $\vec{x}$  such that  $p(\vec{x})$  contains  $a$ .

This is clear when L is SLDD<sub>+</sub> or SLDD<sub>×</sub>: using a shortest path algorithm, we can compute in polynomial time, for any node  $N$ , the cost  $\text{min}_{\text{out}}(N)$  of the cheapest path from  $N$  to the leaf, and the cost  $\text{min}_{\text{in}}(N)$  of the cheapest path from the root to  $N$ . Then for any arc  $a$  in  $\alpha$ , denoting  $M$  and  $N$  its source and destination nodes,  $\text{mincost}(a) = \text{min}_{\text{in}}(M) \otimes \varphi_a \otimes \text{min}_{\text{out}}(N)$ , where  $\otimes$  is + for SLDD<sub>+</sub> and × for SLDD<sub>×</sub>.

In the case of AADD, this is less direct. We use the fact that for any node  $N$  in a normalized AADD formula, there always exist a path from  $N$  to the leaf with valuation 0. This allows the computation of  $\text{mincost}(a)$  for each arc  $a$  in one traversal of the graph from the root to the sink in topological order. The procedure is described in Algorithm 4; the idea is to compute an “offset” for each node  $N$ , representing

the aggregation of the valuation pairs of each arc along the cheapest path from the root to  $N$ .

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**Algorithm 4:** MinCostArcsAADD( $\alpha$ )

---

```

input : an AADD formula  $\alpha$ , of root  $R$ , with offset
        $\langle q_0, f_0 \rangle$ 
output: the value of  $\text{mincost}(a)$  for each arc  $a$  in  $\alpha$ 

1 foreach node  $N$  of  $\alpha$  in reverse topological ordering do
2   if  $N$  is the root node then
3     let  $q_{\text{min}}(N) := q_0$ 
4     let  $f_{\text{min}}(N) := f_0$ 
5   else
6     let  $a_{\text{min}} := \arg \min_{a \in \text{In}(N)} \text{mincost}(a)$ 
7     let  $q_{\text{min}}(N) := q_{\text{min}}(a_{\text{min}})$ 
8     let  $f_{\text{min}}(N) := f_{\text{min}}(a_{\text{min}})$ 
9   foreach arc  $a$  going out of  $N$  do
10    let  $q_{\text{min}}(a) := q_{\text{min}}(N) + f_{\text{min}}(N) * q_a$ 
11    let  $f_{\text{min}}(a) := f_{\text{min}}(N) * f_a$ 
12    let  $\text{mincost}(a) = q_{\text{min}}(a) + f_{\text{min}}(a)$ 
13 return  $\{ \langle a, \text{mincost}(a) \rangle \mid a \text{ arc in } \alpha \}$ 

```

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It is thus possible, for any  $L \in \{\text{SLDD}_+, \text{SLDD}_\times, \text{AADD}\}$ , to obtain in polynomial time the value  $\text{mincost}(a)$  for any arc  $a$  in the L representation  $\alpha'$  of the function  $g = \max(f_\alpha, f_\gamma)$ . With a simple transformation, we can turn  $\alpha'$  into an L representation of the cut  $f_{\leq \gamma}$  on  $\{b, c\}$ .

The procedure is as follows: label the leaf with  $b$ ; add a new  $c$ -labeled leaf; redirect every arc  $a$  such that  $\text{mincost}(a) > \gamma$  to the new  $c$  leaf; remove all arc valuations. The result is an ADD formula, which represents  $f_{\leq \gamma}$ . Indeed, let  $\vec{x} \in \text{CUT}^{\geq \gamma}(f_\alpha)$ ; clearly,  $g(\vec{x}) = \gamma$ , so each arc  $a$  along the path  $p(\vec{x})$  in  $\alpha'$  is such that  $\text{mincost}(a) = \gamma$ : the path is still the same in  $\beta$ , and leads to the  $b$ -leaf.

Conversely, consider an assignment  $\vec{x}$  such that  $f_\beta(\vec{x}) = b$ ; we show that  $\vec{x} \in \text{CUT}^{\geq \gamma}(f_\alpha)$ . Let us denote as  $p_\beta(\vec{x})$  the path corresponding to  $\vec{x}$  in  $\beta$ , and as  $p_{\alpha'}(\vec{x})$  the corresponding path in  $\alpha'$ . It is clear that these two paths contain the same arcs, since  $p_\beta(\vec{x})$  leads to the  $b$ -leaf (no arc has been modified). Denoting  $\langle a_1, \dots, a_n \rangle$  the sequence of arcs along these paths, we know by construction that for each  $a_i$ ,  $\text{mincost}(a_i) = \gamma$ . We show by induction that for all  $k \in \{1, n\}$ , there exists a path starting with  $a_1, \dots, a_k$  of valuation  $\gamma$ .

This is trivial for  $k = 1$ , since  $\text{mincost}(a_1) = \gamma$ . Suppose the hypothesis holds for some  $k \in \{1, n-1\}$ ; we show it holds for  $k+1$ . Let  $p_k$  be the path in  $\alpha'$  from the source node of  $a_{k+1}$  to the leaf such that  $\varphi(\langle a_1, \dots, a_k \rangle \cdot p_k) = \gamma$ , where  $\varphi(p)$  denotes the valuation of some path  $p$ . Since  $\text{mincost}(a_{k+1}) = \gamma$ , there exists a path of valuation  $\gamma$  containing  $a_{k+1}$ : let us denote  $p_{\text{in}}$  the part before  $a_{k+1}$  and  $p_{k+1}$  the part after  $a_{k+1}$ .

By construction, each path  $p$  in  $\alpha'$  verifies  $\varphi(p) \geq \gamma$  (recall that  $\alpha'$  represents  $\max(f_\alpha, f_\gamma)$ ). Hence, we know that

the following inequalities hold:

$$\begin{aligned}\varphi(p_{\text{in}} \cdot p_k) &\geq \gamma \\ \varphi(\langle a_1, \dots, a_k, a_{k+1} \rangle \cdot p_{k+1}) &\geq \gamma\end{aligned}$$

(it can be easily checked that the two paths are legal paths). Now, since  $\varphi(p_{\text{in}} \cdot \langle a_{k+1} \rangle \cdot p_{k+1}) = \gamma$ , we can deduce from the first inequation that  $\varphi(p_k) \geq \varphi(\langle a_{k+1} \rangle \cdot p_{k+1})$ , of which we can deduce that  $\varphi(\langle a_1, \dots, a_k \rangle \cdot p_k) \geq \varphi(\langle a_1, \dots, a_k, a_{k+1} \rangle \cdot p_{k+1})$  (it can be checked that these deductions are valid for any  $L \in \{\text{SLDD}_+, \text{SLDD}_\times, \text{AADD}\}$ ).

Since  $\varphi(\langle a_1, \dots, a_k \rangle \cdot p_k) = \gamma$ , it holds that  $\gamma \geq \varphi(\langle a_1, \dots, a_k, a_{k+1} \rangle \cdot p_{k+1})$ ; combining this result with the second inequation, we get that  $\varphi(\langle a_1, \dots, a_k, a_{k+1} \rangle \cdot p_{k+1}) = \gamma$ : the proposition holds for  $k + 1$ , therefore by induction it holds for all  $k \in \{1, n\}$ . The fact that it holds for  $k = n$  implies that  $\varphi(\langle a_1, \dots, a_n \rangle) = \gamma$ :  $f_{\alpha'}(\vec{x}) = \gamma$ , so  $\vec{x} \in \text{CUT}^{\geq \gamma}(f_\alpha)$ .

This proves that  $\beta$  is an ADD representation of  $f_{\leq \gamma}$  on  $\{b, c\}$ ; it could be transformed in linear time into an equivalent L representation (Proposition A.7). Hence, for  $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , or  $\text{AADD}$ , the satisfaction of  $\text{maxBC}$  implies that of  $\text{CUT}_{\leq \gamma}$ ; since these languages do not satisfy  $\text{CUT}_{\leq \gamma}$ , they cannot satisfy  $\text{maxBC}$ .

It can be shown in a similar way that for these three languages, the satisfaction of  $\text{minBC}$  implies that of  $\text{CUT}_{\geq \gamma}$ , considering the min-combination of  $\alpha$  with an L representation of the constant function  $f_\gamma$ . Since  $\text{CUT}_{\geq \gamma}$  is not satisfied by  $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and  $\text{AADD}$ , we conclude in the same way that these languages do not satisfy  $\text{minBC}$ .  $\square$

### Proposition A.32.

- $\text{SLDD}_+$  and  $\text{AADD}$  do not satisfy  $\times \text{BC}$ ;
- $\text{SLDD}_\times$  and  $\text{AADD}$  do not satisfy  $+ \text{BC}$ .

*Proof.* The proofs of the two items work in a similar way; we first consider the case of  $\times \text{BC}$  on  $\text{SLDD}_+$  and  $\text{AADD}$ .

Let  $f$  be the function of  $n$  Boolean variables (with  $n \geq 3$ ) defined by  $\forall \vec{x}, f(\vec{x}) = \sum_{i=0}^{n-1} x_i \cdot 2^i$  (this is the function associating a  $n$ -bit vector with its corresponding integer), and  $g$  the function of  $n$  Boolean variables defined by  $\forall \vec{x}, g(\vec{x}) = 2^n + \sum_{i=0}^{n-1} (1 - x_i) \cdot 2^i$ . It holds that  $\forall \vec{x}, f(\vec{x}) + g(\vec{x}) = 2^{n+1} - 1$ .

We consider the variable ordering given by  $x_0 \triangleleft x_1 \triangleleft \dots \triangleleft x_{n-1}$ .

Each of the two functions  $f$  and  $g$  has an  $\text{SLDD}_+$  representation with  $n + 1$  nodes and  $2n$  arcs (one node per variable, two arcs per node). In the one of  $f$  (see Figure 3), the node at level  $x_i$  has one arc with label 1 and valuation  $2^i$  and the other with label 0 and valuation 0. In the one of  $g$  (see Figure 4), the arc valuations are inverted at each level, and the root is associated with an offset  $\varphi_0 = 2^n$ .

Let us consider the following ADD representation  $\alpha$  of  $h = f \times g$ . There are  $2^n$  paths in  $\alpha$ ; let us denote them  $p_0, \dots, p_{2^n-1}$ , with  $p_k$  being the one that corresponds to the assignment  $\vec{x}$  such that  $f(\vec{x}) = k$ . The path  $p_k$  leads to a leaf of value  $k(2^{n+1} - k - 1)$ , that we denote as  $h_k$ . The sequence  $\langle h_k \rangle_{0 \leq k < 2^n}$  is strictly increasing (since  $k \leq 2^{n-1}$ ,  $\delta_k = h_k - h_{k-1} = 2(2^n - k)$ ,  $\delta_k > 4$  for  $0 \leq k \leq 2^{n-1}$ ),

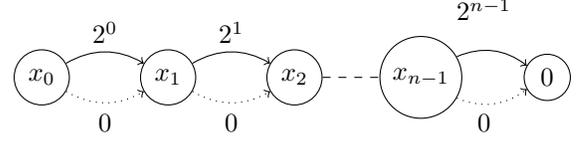


Figure 3: A  $\text{SLDD}_+$  representation of  $f$

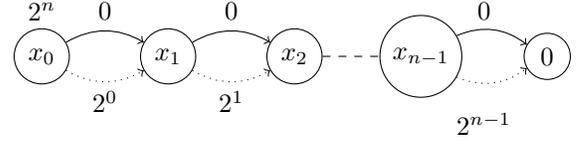


Figure 4: A  $\text{SLDD}_+$  representation of  $g$

so all leaves are labeled with a different value. Now, let us build a  $\text{AADD}$  representation of  $h$  from  $\alpha$ . Following the procedure outlined in the proof of Proposition A.7, we start with the  $\text{ADD}$  formula  $\alpha$ , and add a neutral valuation  $\langle 0, 1 \rangle$  to every arc except for those pointing to a leaf  $L$ , which receive the value  $\langle \varphi(L), 0 \rangle$ . Stated otherwise, the last arc of each path  $p_k$  is valued  $\langle h_k, 0 \rangle$ . Then the leaves are merged into a unique leaf, and the  $\text{AADD}$  formula obtained is normalized, following Algorithm 1. Let us suppose the algorithm is treating some node at level  $x_{n-1}$  (the level closest to the leaf); it is traversed by two paths, say  $p_{k-1}$  and  $p_k$ . We have  $q_{\text{min}} = h_{k-1}$  and  $\text{range} = h_k - h_{k-1} = \delta_k$ , which is greater than 0, so the  $h_{k-1}$  arc receives value  $\langle 0, 0 \rangle$ , the  $h_k$  arc receives value  $\langle 1, 0 \rangle$ , and the unique incoming arc receives value  $\langle h_{k-1}, h_k - h_{k-1} \rangle$ . Clearly, all nodes at level  $x_{n-1}$  are isomorphic but none of them is redundant.

Now, let us suppose the algorithm is treating some node  $N$  at level  $x_{n-2}$ . It is traversed by four paths, say  $p_{4j}, p_{4j+1}, p_{4j+2}$ , and  $p_{4j+3}$ . Its two outgoing arcs are respectively valued with  $\langle h_{4j}, \delta_{4j+1} \rangle$  and  $\langle h_{4j+2}, \delta_{4j+3} \rangle$ . In this case,  $q_{\text{min}} = h_{4j}$  and  $\text{range} = h_{4j+3} - h_{4j}$ . Since  $\text{range} > 0$ , the 0-labeled outgoing arc of  $N$  receives the value  $\langle 0, \delta_{4j+1}/\text{range} \rangle$ . Let us denote  $v_j$  the multiplicative constant of this valuation:  $v_j = \delta_{4j+1}/(h_{4j+3} - h_{4j})$ .

It is tedious, yet not difficult, to check that the sequence  $\langle v_j \rangle_{0 \leq j \leq \lfloor (2^n - 1)/4 \rfloor}$  is strictly increasing (recall that we suppose  $n \geq 3$ ). Hence, the 0-labeled outgoing arcs of all nodes at level  $x_{n-2}$  are all valued with a distinct multiplicative constant: none of them can be isomorphic to another one. Furthermore, the 1-labeled outgoing arc of  $N$  receives the value  $\langle (h_{4j+2} - h_{4j})/(h_{4j+3} - h_{4j}), (h_{4j+3} - h_{4j+2})/(h_{4j+3} - h_{4j}) \rangle$ . Since  $h_{4j+2} - h_{4j} > 0$ , this label differs from the one of the 0-labeled outgoing arc of  $N$ , hence  $N$  is not redundant.

Consequently, after applying the normalization and the reduction procedures, the resulting  $\text{AADD}$  formula has at least  $\lfloor (2^n - 1)/4 \rfloor + 1 = 2^{n-2}$  distinct nodes at level  $x_{n-2}$ . Recall that no  $\text{AADD}$  formula can be strictly smaller than an equivalent reduced and normalized  $\text{AADD}$  formula (Proposition A.6). All in all, we have the following:

- (i)  $f$  and  $g$  have  $\text{SLDD}_+$  representations of size polynomial in  $n$ ;
- (ii) the size of the smallest AADD representation of  $f \times g$  is exponential in  $n$ .

Since any  $\text{SLDD}_+$  representation can be transformed into an AADD formula in linear time (Proposition A.7):

- (a)  $f$  and  $g$  have an AADD representation of size polynomial in  $n$ , because of (i);
- (b) the size of the smallest  $\text{SLDD}_+$  representation of  $f \times g$  is exponential in  $n$ , because of (ii).

To sum up,  $\text{SLDD}_+$  does not satisfy  $\times\text{BC}$  (thanks to (i) and (b)), and AADD does not satisfy  $\times\text{BC}$  either (thanks to (ii) and (a)).

Now, for  $+\text{BC}$  on  $\text{SLDD}_\times$  and AADD, the proof is very similar to the previous one, using different functions  $f$  and  $g$ : we take  $f(\vec{x}) = 2^{\sum_{i=0}^{n-1} x_i \cdot 2^i}$ , and  $g$  such that  $f(\vec{x}) \times g(\vec{x}) = 2^{2^{n+1}-1}$ .

Both functions have  $\text{SLDD}_\times$  representations with  $n+1$  nodes and  $2n$  arcs, and only exponential AADD representations (following the same mechanism as in the previous proof). Since a  $\text{SLDD}_\times$  representation can be transformed into an AADD one in linear time (Proposition A.7), the results follow.  $\square$

### Proposition A.33.

- $\text{SLDD}_+$  satisfies  $+\text{BC}$ ;
- $\text{SLDD}_\times$  satisfies  $\times\text{BC}$ ;
- ADD satisfies  $\times\text{BC}, +\text{BC}, \max\text{BC}, \min\text{BC}$ .

*Proof.* Let  $\alpha$  and  $\alpha'$  be two  $\text{SLDD}_\otimes$  formulæ, over  $\{x_1, \dots, x_n\}$ , with  $\otimes \in \{+, \times\}$ . They are supposed to be ordered in the same way, using the ordering  $x_1 \triangleleft \dots \triangleleft x_n$ . We aim at building an  $\text{SLDD}_\otimes$  representation of  $g = f_\alpha^{\text{SLDD}_\otimes} \otimes f_{\alpha'}^{\text{SLDD}_\otimes}$  based on the same variable ordering.

First, we are going to modify  $\alpha$  (resp.  $\alpha'$ ) such that every path from the root to the leaf of the formula is of the form  $\langle a_1, \dots, a_n \rangle$  where each  $a_i$  (with  $i \in \{1, \dots, n-1\}$ ) is an arc from a node labeled with  $x_i$  to a node labeled with  $x_{i+1}$ , and  $a_n$  is an arc from a node labeled with  $x_n$  to the leaf node. Lemma A.24 states that this can be done in polynomial time.

Consider now an assignment  $\vec{x}$  and let  $p(\vec{x}) = \langle a_1, \dots, a_n \rangle$  and  $p'(\vec{x}) = \langle a'_1, \dots, a'_n \rangle$  be the corresponding paths in  $\alpha$  and  $\alpha'$ . It holds that:

$$g(\vec{x}) = (\varphi(a_1) \otimes \dots \otimes \varphi(a_n)) \otimes (\varphi(a'_1) \otimes \dots \otimes \varphi(a'_n)).$$

Because  $\otimes$  is associative and commutative, we get:

$$g(\vec{x}) = (\varphi(a_1) \otimes \varphi(a'_1)) \otimes \dots \otimes (\varphi(a_n) \otimes \varphi(a'_n)).$$

It is thus possible to get an  $\text{SLDD}_\otimes$  representation of  $g$  by making the product of the two graphs, levelwise: for any  $N$  of  $\alpha$  and  $N'$  of  $\alpha'$  such that  $\text{Var}(N) = \text{Var}(N') = x$ , the new graph,  $\delta$ , contains the node denoted  $N \otimes N'$  such that  $\text{Var}(N \otimes N') = x$ ; we add a leaf labeled with the neutral element for  $\otimes$ . For each value  $d$  in the domain of  $x$ , let  $a$  be the arc in  $\text{Out}(N)$  such that  $v(a) = d$  and  $a'$  be the arc in  $\text{Out}(N')$  such that  $v(a') = d$ ; add to the new graph

one arc  $a_\delta$  from node  $N \otimes N'$  to node  $M \otimes M'$  with value  $v(a_\delta) = d$  and  $\varphi(a_\delta) = \varphi(a) \otimes \varphi(a')$ ; then recursively delete unreachable nodes and arcs. The offset of  $\delta$  is set to  $\text{Offset}(\alpha) \otimes \text{Offset}(\alpha')$ . This construction is feasible in time polynomial in the sizes of  $\alpha$  and  $\alpha'$ .

The resulting structure  $\delta$  is a (typically non-normalized)  $\text{SLDD}_\otimes$  formula. For any assignment  $\vec{x}$ , consider the corresponding path  $p(\vec{x}) = \langle a''_1, \dots, a''_n \rangle$  in  $\delta$ . We have:

$$\begin{aligned} f_\delta^{\text{SLDD}_\otimes}(\vec{x}) &= \text{Offset}(\delta) \otimes \varphi(a''_1) \otimes \dots \otimes \varphi(a''_n) \\ &= \text{Offset}(\alpha) \otimes \text{Offset}(\alpha') \\ &\quad \otimes \varphi(a_1) \otimes \varphi(a'_1) \otimes \dots \otimes \varphi(a_n) \otimes \varphi(a'_n) \\ &= (\text{Offset}(\alpha) \otimes \varphi(a_1) \otimes \dots \otimes \varphi(a_n)) \\ &\quad \otimes (\text{Offset}(\alpha') \otimes \varphi(a'_1) \otimes \dots \otimes \varphi(a'_n)) \\ &= f_\alpha^{\text{SLDD}_\otimes}(\vec{x}) \otimes f_{\alpha'}^{\text{SLDD}_\otimes}(\vec{x}) \\ &= (f_\alpha^{\text{SLDD}_\otimes} \otimes f_{\alpha'}^{\text{SLDD}_\otimes})(\vec{x}). \end{aligned}$$

That is to say,  $\delta$  is an  $\text{SLDD}_+$  representation of  $f_\alpha^{\text{SLDD}_\otimes} \otimes f_{\alpha'}^{\text{SLDD}_\otimes}$ . This proves that  $\text{SLDD}_+$  satisfies  $+\text{BC}$  and  $\text{SLDD}_\times$  satisfies  $\times\text{BC}$ .

Bounded combinations by  $\otimes \in \{\min, \max, +, \times\}$  hold on ADD formulæ, using the same idea of automata product. There are no valuations on the arcs, but on the leaf nodes: for each leaf  $N$  in  $\alpha$  and each leaf  $M$  in  $\beta$ , the leaves of  $\delta$  are nodes  $N \otimes M$  labeled with  $\varphi(N \otimes M) = \varphi(N) \otimes \varphi(M)$ .  $\square$

## Sentential Entailment

### Proposition A.34. ADD satisfies SE.

*Proof.* Let  $\alpha$  and  $\beta$  be two ADD formulæ. We have to show that it is possible to decide in polynomial time whether  $\forall \vec{x}, f_\alpha(\vec{x}) \leq f_\beta(\vec{x})$ . The point is that this property holds if and only if for every value  $\gamma$  taken by  $f_\alpha$  on some assignments  $\vec{x}$ , the set of these assignments is included in the set of assignments  $\vec{y}$  such that  $f_\beta(\vec{y}) \geq \gamma$ . So, let  $\Gamma$  be the (finite) set of labels of the terminal nodes of  $\alpha$ . For each level  $\gamma \in \Gamma$ , thanks to Propositions A.11 and A.8, we compute in linear time an MDD formula  $\alpha_{=\gamma}$  representing  $CUT^{\sim\gamma}(f_\alpha)$  (its 1-leaf is the leaf of  $\alpha$  labeled with  $\gamma$  and its 0-leaf is obtained by merging all the leaves of  $\alpha$  not labeled with  $\gamma$ ) and an MDD formula  $\beta_{\geq\gamma}$  representing  $CUT^{\geq\gamma}(f_\beta)$  (its 1-leaf is obtained by merging all the leaves of  $\beta$  labeled with a  $\lambda \geq \gamma$  and its 0-leaf is obtained by merging all the leaves of  $\beta$  not labeled with a  $\lambda < \gamma$ ). Checking whether every model of  $\alpha_{=\gamma}$  is a model of  $\beta_{\geq\gamma}$  can be done in polynomial time since **SE** is satisfied by MDD.

The procedure repeats this operation for each element of  $\Gamma$ , thus it runs in polynomial time. If it is the case that for each  $\gamma \in \Gamma$ , every model of  $\alpha_{=\gamma}$  is a model of  $\beta_{\geq\gamma}$ , then it means that  $\forall \vec{x}, f_\alpha(\vec{x}) \leq f_\beta(\vec{x})$ , so the procedure outputs 1; if it is not the case, it outputs 0.  $\square$

### Proposition A.35. $\text{SLDD}_\times$ satisfies SE.

*Proof.* First, note that  $\forall \vec{x}, f(\vec{x}) \leq g(\vec{x})$  holds if and only if (i)  $\forall \vec{x}, g(\vec{x}) = 0 \implies f(\vec{x}) = 0$ , and (ii)  $\forall \vec{x}, f \times$

$g'(\vec{x}) \leq 1$ , where  $g'(\vec{x}) = 1/g(\vec{x})$  if  $g(\vec{x}) > 0$  and 0 otherwise; this is not hard to check in both directions. Then, testing whether  $\forall \vec{x}, f_\alpha(\vec{x}) \leq f_\beta(\vec{x})$  amounts to verify that both conditions hold.

- (i) To verify that  $\forall \vec{x}, f_\beta(\vec{x}) = 0 \implies f_\alpha(\vec{x}) = 0$ , we only have to compute the minimal value taken by  $f_\beta$  (this is polynomial, since  $\text{SLDD}_\times$  satisfies  $\text{OPT}_{\min}$ , see Proposition A.13), and if it is 0, to compute MDD formulæ  $\alpha'$  and  $\beta'$  representing the min-cuts of  $\alpha$  and  $\beta$ , respectively (this is polynomial, since by Proposition A.14,  $\text{SLDD}_\times$  satisfies  $\text{CUT}_{\min}$ , and by Proposition A.8, an  $\text{SLDD}_\times$  formula on  $\{0, 1\}$  can be transformed into an equivalent MDD in polynomial time), and to check whether  $\beta' \models \alpha'$ , again in polynomial time as MDD satisfies **SE** (Amilhastre et al. 2012).
- (ii) To verify that  $\forall \vec{x}, f \times g'(\vec{x}) \leq 1$ , we first compute an  $\text{SLDD}_\times$  representation  $\delta$  of  $g'$  by inverting the offset and the label of every arc in  $f_\beta$  (that is,  $V$  becomes  $1/V$ ), except for 0-labels that remain unchanged (any path in  $\delta$  corresponds to the value  $\prod_i 1/\varphi(a_i) = 1/\prod_i \varphi(a_i)$ , except when it contains a 0-arc, so  $f_\delta = g'$ ). It is then easy to check whether  $\forall \vec{x}, f_\alpha(\vec{x}) \times g'(\vec{x}) \leq 1$ : just compute the  $\times$ -combination of  $\alpha$  and  $\delta$  (this is polynomial because  $\text{SLDD}_\times$  satisfies  $\times\text{BC}$ , see Proposition A.33), compute the maximal value  $v^*$  taken by the resulting formula (polynomial because  $\text{SLDD}_\times$  satisfies  $\text{OPT}_{\max}$ , see Proposition A.13) and check whether  $v^* \leq 1$ .  $\square$

**Proposition A.36.**  $\text{SLDD}_+$  satisfies **SE**.

*Proof.* First, note that  $\forall \vec{x}, f(\vec{x}) \leq g(\vec{x})$  holds if and only if  $\forall \vec{x}, K \leq g(\vec{x}) + K - f(\vec{x})$ , where  $K$  is some constant. Thus, in order to check whether  $\forall \vec{x}, f_\alpha(\vec{x}) \leq f_\beta(\vec{x})$ , we are going to build an  $\text{SLDD}_+$  formula  $\alpha'$  such that  $f_{\alpha'} = K - f_\alpha$ , with  $K$  large enough for  $f_{\alpha'}$  to range over  $\mathbb{R}^+$ . We compute  $v^*$ , the maximal value taken by  $f_\alpha$  (this can be done in polynomial time, since  $\text{SLDD}_+$  satisfies  $\text{OPT}_{\max}$ , see Proposition A.13), and then choose some  $K \geq v^*$ . Applying Lemma A.25, we get an  $\text{SLDD}_+$  representation  $\alpha'$  of the function  $K - f_\alpha$ . We then only have to build the  $+$ -combination of  $\alpha'$  and  $\beta$  (polynomial since  $\text{SLDD}_+$  satisfies  $+\text{BC}$ , see Proposition A.33) and check that the minimal value taken by the resulting formula is larger than  $K$  (polynomial since  $\text{SLDD}_+$  satisfies  $\text{OPT}_{\min}$ , see Proposition A.13).  $\square$

## Variable Elimination

**Lemma A.37.** For each language  $L$  among  $\{\text{ADD}, \text{SLDD}_+, \text{SLDD}_\times, \text{AADD}\}$ , and each operator  $\odot \in \{\max, \min, +, \times\}$ ,

- $L$  satisfies **S $\odot$ Elim** if and only if it satisfies  $\odot\text{C}$ ;
- $L$  satisfies **SB $\odot$ Elim** if and only if it satisfies  $\odot\text{BC}$ ;

*Proof.* We first prove the two sufficient conditions, then the two necessary conditions.

( $\implies$ ) If the  $\odot$ -elimination of a single variable in an L formula  $\Sigma$  can be achieved in polynomial time, it is possible

to compute in polynomial time the  $\odot$ -combination of any set  $\{\alpha_1, \dots, \alpha_n\}$  of L formulæ. Indeed, from  $\{\alpha_1, \dots, \alpha_n\}$ , we can generate in linear time the following L formula  $\Sigma$ : its root  $N_0$  is labeled with a (new) variable  $v$  not occurring in any  $\alpha_i$ , with  $D_v = \{1, \dots, n\}$ ;  $N_0$  has  $n$  outgoing arcs  $a_i$  (with  $i \in \{1, \dots, n\}$ ), where each  $a_i$  corresponds to  $v = i$  and points to the root of  $\alpha_i$ , and the leaves of  $\alpha_1, \dots, \alpha_n$  are merged when they have the same label; finally, when  $L = \text{AADD}$  (resp.  $\text{SLDD}_+$ , resp.  $\text{SLDD}_\times$ ), the label of each  $a_i$  is set to  $\langle 0, 1 \rangle$  (resp. 0, resp. 1). By definition, the  $\odot$ -elimination of  $v$  in  $\Sigma$  is  $f_{\Sigma, \langle v, 1 \rangle}^L \odot \dots \odot f_{\Sigma, \langle v, n \rangle}^L$ , which by construction is equal to  $f_{\alpha_1}^L \odot \dots \odot f_{\alpha_n}^L$ . Accordingly, if we can obtain in polynomial time an L representation of the  $\odot$ -elimination of  $v$  in  $\Sigma$ , we can also obtain in polynomial time an L representation of the  $\odot$ -combination of  $\{\alpha_1, \dots, \alpha_n\}$ .

This proves that the satisfaction of **S $\odot$ Elim** implies that of  $\odot\text{C}$ ; now, note that the exact same construction also works in the bounded case. Indeed, when  $n = 2$ , the  $\odot$ -elimination of  $v$  in  $\Sigma$ , which can be obtained in time polynomial in  $|\Sigma|^2$  if  $L$  satisfies **SB $\odot$ Elim**, is  $f_{\Sigma, \langle v, 1 \rangle}^L \odot f_{\Sigma, \langle v, 2 \rangle}^L$ , which by construction is equal to  $f_{\alpha_1}^L \odot f_{\alpha_2}^L$ . This proves that the satisfaction of **SB $\odot$ Elim** implies that of  $\odot\text{BC}$ .

( $\Leftarrow$ ) Consider an L formula  $\alpha$  and a variable  $x \in \text{Var}(\alpha)$  with a domain of size  $d$ . Since  $L$  satisfies **CD** (Proposition A.10),<sup>6</sup> we can build in time polynomial an L representation of  $f_{\alpha, \vec{x}}^L$  for each of the  $d$  possible  $\vec{x}$ ; we denote them as  $\alpha_1, \dots, \alpha_d$ .

Now, if  $L$  satisfies  $\odot\text{C}$ , we can obtain an L representation  $\beta$  of  $\odot_{\vec{x}} f_{\alpha, \vec{x}}^L$  in time polynomial in  $\sum_{i=1}^d |\alpha_i|$ , which is polynomial in  $|\alpha|$  (each  $\alpha_i$  is of size polynomial in  $|\alpha|$ , and  $d \leq |\alpha|$  since there is at least one  $x$ -node in  $\alpha$ , that has by definition  $d$  outgoing arcs). By definition,  $\beta$  is an L representation of the  $\odot$ -elimination of  $x$  in  $\alpha$ , so  $L$  satisfies  $\odot\text{C}$ .

If  $L$  only satisfies  $\odot\text{BC}$ , then we can also build  $\beta$ , but we have to do it incrementally with  $d - 1$  binary  $\odot$ -combinations, so the bound is looser:  $\alpha_1 \odot \dots \odot \alpha_d$  is obtained in time bounded by a polynomial of  $|\alpha|^d$ . However, this is enough for  $L$  to satisfy **SB $\odot$ Elim**, by definition.  $\square$

**Proposition A.38.**  $\text{ADD}$ ,  $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and  $\text{AADD}$  do not satisfy **S $\odot$ Elim** or  $\odot\text{Elim}$  for any  $\odot \in \{\max, \min, +, \times\}$ .

*Proof.* For any  $\odot \in \{\max, \min, +, \times\}$ , the satisfaction of  $\odot\text{Elim}$  implies that of **S $\odot$ Elim**, and the satisfaction of **S $\odot$ Elim** implies that of  $\odot\text{C}$  (Lemma A.37); yet, we have shown that  $\text{ADD}$ ,  $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and  $\text{AADD}$  do not satisfy  $\odot\text{C}$  when  $\odot \in \{\max, \min, +, \times\}$  (Proposition A.30).  $\square$

**Proposition A.39.** The following results hold:

- $\text{ADD}$  satisfies **SB $\odot$ Elim** for  $\odot \in \{\times, +, \min, \max\}$ ;
- $\text{SLDD}_+$  satisfies **SB+Elim**;
- $\text{SLDD}_+$  and  $\text{AADD}$  do not satisfy **SB $\times$ Elim**;
- $\text{SLDD}_\times$  satisfies **SB $\times$ Elim**;
- $\text{SLDD}_\times$  and  $\text{AADD}$  do not satisfy **SB+Elim**;

<sup>6</sup>Note that conditioning can actually be achieved in linear time, without ever increasing the size of the formula. But the proof would work even if it were not the case.

- $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and  $\text{AADD}$  do not satisfy  $\text{SB}\odot\text{Elim}$  for  $\odot \in \{\max, \min\}$ .

*Proof.* These are direct consequences of Lemma A.37, since

- $\text{ADD}$  satisfies  $\odot\text{BC}$  for  $\odot \in \{\times, +, \min, \max\}$  (Proposition A.33);
- $\text{SLDD}_+$  satisfies  $+\text{BC}$  (Proposition A.33);
- $\text{SLDD}_+$  and  $\text{AADD}$  do not satisfy  $\times\text{BC}$  (Proposition A.32);
- $\text{SLDD}_\times$  satisfies  $\times\text{BC}$  (Proposition A.33);
- $\text{SLDD}_\times$  and  $\text{AADD}$  do not satisfy  $+\text{BC}$  (Proposition A.32);
- $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and  $\text{AADD}$  do not satisfy  $\max\text{BC}$  or  $\min\text{BC}$  (Proposition A.31).  $\square$

### Marginalization

The marginalization proofs use the fact that the  $\odot$ -elimination of every variable in any L formula  $\alpha$  (i.e., the “full” variable elimination) can be done in polynomial time. We denote as  $\text{Elim}_\odot(\alpha)$  the value resulting from such a “full” variable elimination, that is, the value  $\bigodot_{\vec{x} \in D_{\text{Var}_L(\alpha)}} f_\alpha^L(\vec{x})$ .

**Lemma A.40.** *For any language  $L \in \{\text{ADD}, \text{SLDD}_+, \text{SLDD}_\times, \text{AADD}\}$  and any operator  $\odot \in \{\max, \min, +, \times\}$ , if there exists a polynomial algorithm mapping any L formula  $\alpha$  to the value  $\text{Elim}_\odot(\alpha)$ , then L satisfies  $\odot\text{Marg}$ .*

*Proof.* Consider an L formula  $\alpha$  and a variable  $x$ , denoting denoting  $D_x = \{d_1, \dots, d_k\}$ . The following ADD formula  $\beta$  is a representation of the  $\odot$ -marginalization of  $f_\alpha^L$  on  $x$ :  $\beta$  has one root labeled with  $x$ , with  $k$  outgoing arcs  $a_1, \dots, a_k$ , such that for each  $i \in \{1, \dots, k\}$ ,  $v(a_i) = d_i$  and  $a_i$  points to a leaf  $L_i$  with value  $\varphi(L_i) = \bigodot_{\vec{y} \in D_{\text{Var}(\alpha) \setminus \{x\}}} f_{\alpha, \vec{y}}^L(\langle x, d_i \rangle)$ , that is,  $\varphi(L_i) = \text{Elim}_\odot(f_{\alpha, \langle x, d_i \rangle}^L)$ . Accordingly, if for any value  $d_i$ , the valuation  $\varphi(L_i)$  can be computed in time polynomial in the size of  $\alpha$  (i.e., if “full” variable elimination can be computed in polynomial time from  $\alpha$  once conditioned by  $x = d_i$ ), then  $\beta$  can be computed in time polynomial in the size of  $\alpha$ .

Since all four languages satisfy  $\text{CD}$  (Proposition A.10), this implies that if there exists a polynomial algorithm for “full” variable elimination, then there exists a polynomial algorithm building an ADD representation  $\beta$  of the marginalization of any L formula on  $x$ . This shows the result when L is ADD; now when L is  $\text{SLDD}_+$  (resp.  $\text{SLDD}_\times$ , resp.  $\text{AADD}$ ),  $\beta$  can be turned in linear time into an equivalent  $\text{SLDD}_+$  (resp.  $\text{SLDD}_\times$ , resp.  $\text{AADD}$ ) formula (Proposition A.7).  $\square$

**Proposition A.41.** *ADD,  $\text{SLDD}_+$ ,  $\text{SLDD}_\times$ , and  $\text{AADD}$  satisfy  $\odot\text{Marg}$  for  $\odot \in \{\max, \min\}$ .*

*Proof.* When  $\odot$  is  $\max$  (resp.  $\min$ ),  $\text{Elim}_\odot(\alpha)$  is simply the maximal (resp. minimal) value taken by  $f_\alpha^L$ . Since from Proposition A.13, each language in  $\{\text{ADD}, \text{SLDD}_+, \text{SLDD}_\times, \text{AADD}\}$  satisfies  $\text{OPT}_{\max}$  (resp.  $\text{OPT}_{\min}$ ), “full” variable  $\max$ -elimination (resp.  $\min$ -elimination) can be done in polynomial time; hence from Lemma A.40 we get that these languages all satisfy  $\max\text{Marg}$  (resp.  $\min\text{Marg}$ ).  $\square$

**Proposition A.42.** *There exists a polynomial-time algorithm mapping any  $\text{AADD}$  formula  $\alpha$  to  $\text{Elim}_+(\alpha)$ .*

*Proof.* We show that “full” variable  $+$ -elimination on  $\text{AADD}$  is polynomial because we can iteratively eliminate the last variable in linear time.

Let  $X = \{x_1, \dots, x_n\} \subseteq \mathcal{X}$  and  $y \in \mathcal{X}$ ; let  $\alpha$  be an  $\text{AADD}$  formula over  $X \cup \{y\}$ , ordered in such a way that  $x_1 \triangleleft \dots \triangleleft x_n \triangleleft y$ . To simplify the proof, we suppose that every variable in  $X \cup \{y\}$  is mentioned in every path of  $\alpha$ ; this is harmless, since we never need to add more than  $(n+1)d$  arcs (with  $\varphi$ -value  $\langle 0, 1 \rangle$  per node in  $\alpha$  (with  $d$  the cardinal of the largest variable domain)).

We have the following:

$$\begin{aligned} \text{Elim}_+(\alpha) &= \sum_{\vec{z} \in D_X \times D_y} f_\alpha^{\text{AADD}}(\vec{z}) \\ &= \sum_{\vec{x} \in D_X} \sum_{\vec{y} \in D_y} f_\alpha^{\text{AADD}}(\vec{x} \cdot \vec{y}) \end{aligned}$$

For a given assignment  $\vec{x} \cdot \vec{y}$ , let us consider the path  $p$  in  $\alpha$  corresponding to  $\vec{x} \cdot \vec{y}$ . The path  $p$  contains  $n+1$  arcs, that we denote  $a_1, \dots, a_n, a_{n+1}$ . For each  $i \in \{1, \dots, n+1\}$ , we denote  $\varphi(a_i) = \langle q_i, f_i \rangle$ ; finally, the offset is as usual  $\langle q_0, f_0 \rangle$ . By definition of the interpretation function of  $\text{AADD}$ ,

$$\begin{aligned} f_\alpha^{\text{AADD}}(\vec{x} \cdot \vec{y}) &= q_0 + f_0(q_1 + f_1(q_2 + \dots \\ &\quad \dots + f_n(q_{n+1} + f_{n+1} \times 0) \dots)) \\ &= q_0 + f_0 q_1 + f_0 f_1 q_2 + \dots \\ &\quad \dots + f_0 \dots f_n q_{n+1} \\ &= \sum_{i=0}^{n+1} \left( q_i \prod_{j=0}^{i-1} f_j \right) \\ &= \sum_{i=0}^n \left( q_i \prod_{j=0}^{i-1} f_j \right) + q_{n+1} \cdot \prod_{j=0}^n f_j. \end{aligned}$$

There are three elements in this formula:

- the valuation  $q_{n+1}$  of the  $y$ -arc, which is the last arc along the path, and which we denote as  $\varphi_{\vec{x} \cdot \vec{y}}$ ;
- the “additive offset” given by the first  $n$  arcs,  $\sum_{i=0}^n \left( q_i \prod_{j=0}^{i-1} f_j \right)$ , which we denote as  $Q_{\vec{x}}$ ;
- the “multiplicative offset” given by the first  $n$  arcs,  $\prod_{j=0}^n f_j$ , that we denote as  $F_{\vec{x}}$ .

The key point is that neither  $Q_{\vec{x}}$  nor  $F_{\vec{x}}$  depends on  $\vec{y}$ , so we can write

$$\begin{aligned} \text{Elim}_+(\alpha) &= \sum_{\vec{x} \in D_X} \sum_{\vec{y} \in D_y} f_\alpha^{\text{AADD}}(\vec{x} \cdot \vec{y}) \\ &= \sum_{\vec{x} \in D_X} \sum_{\vec{y} \in D_y} (Q_{\vec{x}} + \varphi_{\vec{x} \cdot \vec{y}} \cdot F_{\vec{x}}) \\ &= \sum_{\vec{x} \in D_X} \left( |D_y| \cdot Q_{\vec{x}} + F_{\vec{x}} \cdot \sum_{\vec{y} \in D_y} \varphi_{\vec{x} \cdot \vec{y}} \right). \end{aligned} \tag{1}$$

It is thus possible to transform  $\alpha$  into another AADD formula  $\alpha'$  that does not mention  $y$  and verifies  $\text{Elim}_+(\alpha') = \text{Elim}_+(\alpha)$  as follows. For each  $y$ -labeled node  $N$ , we compute the value  $\varphi_N = \sum_{a \in \text{Out}(N)} q_a$ . Then we update the  $\varphi$ -label of each arc  $a_{\text{In}} \in \text{In}(N)$  so that it becomes  $\langle q_{a_{\text{In}}} + \frac{f_{a_{\text{In}}}}{|D_y|} \varphi_N, 0 \rangle$ . We also modify the offset, which becomes  $\langle q_0 \cdot |D_y|, f_0 \cdot |D_y| \rangle$ . Finally, we merge all the  $y$ -nodes into a new leaf.

We show that  $\text{Elim}_+(\alpha') = \text{Elim}_+(\alpha)$ ; let us denote  $\langle q', f' \rangle$  the  $\varphi$ -label of an arc in  $\alpha'$  when the corresponding arc in  $\alpha$  has label  $\langle q, f \rangle$ . We can rewrite the left part of the sum in Equation 1:

$$\begin{aligned} |D_y| \cdot Q_{\vec{x}} &= |D_y| \cdot \sum_{i=0}^n \left( q_i \prod_{j=0}^{i-1} f_j \right) \\ &= |D_y| \cdot q_0 + \sum_{i=1}^n \left( q_i \cdot |D_y| \cdot f_0 \prod_{j=1}^{i-1} f_j \right) \\ &= q'_0 + \sum_{i=1}^n \left( q_i f'_0 \prod_{j=1}^{i-1} f_j \right) \\ &= q'_0 + \sum_{i=1}^n \left( q_i \prod_{j=0}^{i-1} f'_j \right), \end{aligned}$$

since for  $i \in \{1, \dots, n-1\}$ ,  $f'_i = f_i$ . Isolating  $q_n$ , we get

$$\begin{aligned} |D_y| \cdot Q_{\vec{x}} &= q'_0 + \sum_{i=1}^{n-1} \left( q_i \prod_{j=0}^{i-1} f'_j \right) + q_n \prod_{j=0}^{n-1} f'_j \\ &= \sum_{i=0}^{n-1} \left( q'_i \prod_{j=0}^{i-1} f'_j \right) + q_n \prod_{j=0}^{n-1} f'_j, \end{aligned}$$

since for  $i \in \{1, \dots, n-1\}$ ,  $q'_i = q_i$ . As for the right part of the sum in Equation 1:

$$\begin{aligned} F_{\vec{x}} \cdot \sum_{\vec{y} \in D_y} \varphi_{\vec{x} \cdot \vec{y}} &= f_0 \left( \prod_{j=1}^{n-1} f_j \right) f_n \sum_{\vec{y} \in D_y} \varphi_{\vec{x} \cdot \vec{y}} \\ &= \left( \prod_{j=0}^{n-1} f'_j \right) \frac{f_n}{|D_y|} \sum_{\vec{y} \in D_y} \varphi_{\vec{x} \cdot \vec{y}}, \end{aligned}$$

since  $f'_0 = f_0 \cdot |D_y|$  and for  $i \in \{1, \dots, n-1\}$ ,  $f'_i = f_i$ . Now, since  $q'_n = q_n + \frac{f_n}{|D_y|} \sum_{\vec{y} \in D_y} \varphi_{\vec{x} \cdot \vec{y}}$ , by summing the two parts, we get

$$\begin{aligned} |D_y| \cdot Q_{\vec{x}} + F_{\vec{x}} \cdot \sum_{\vec{y} \in D_y} \varphi_{\vec{x} \cdot \vec{y}} &= \sum_{i=0}^{n-1} \left( q'_i \prod_{j=0}^{i-1} f'_j \right) + \left( \prod_{j=0}^{n-1} f'_j \right) q'_n \\ &= \sum_{i=0}^n \left( q'_i \prod_{j=0}^{i-1} f'_j \right) = f_{\alpha'}^{\text{AADD}}(\vec{x}), \end{aligned}$$

by definition of the interpretation function of AADD. Equation 1 can hence be rewritten as

$$\text{Elim}_+(\alpha) = \sum_{\vec{x} \in D_X} f_{\alpha'}^{\text{AADD}}(\vec{x}) = \text{Elim}_+(\alpha').$$

The procedure thus eliminates the last variable without changing the value of the “full” variable elimination; moreover, it runs in linear time, and the resulting formula is always smaller than the original one. Hence, iteratively repeating the procedure for each variable in a bottom-up way, and stopping when the resulting formula only contains a leaf and an offset, we obtain  $\text{Elim}_+(\alpha)$  in time polynomial in the size of  $\alpha$ .  $\square$

**Corollary A.43.** ADD, SLDD $_+$ , SLDD $_×$ , and AADD satisfy +Marg.

*Proof.* Full variable +-elimination is polynomial on AADD. Since any ADD (resp. SLDD $_+$ , SLDD $_×$ ) formula can be turned into an equivalent AADD formula in linear time (Proposition A.7), full variable +-elimination is also polynomial on ADD (resp. SLDD $_+$ , SLDD $_×$ ). Hence, from Lemma A.40, we get that all four languages satisfy +Marg.  $\square$

**Proposition A.44.** There exists a polynomial-time algorithm mapping any SLDD $_×$  formula  $\alpha$  to  $\text{Elim}_\times(\alpha)$ .

*Proof.* The proof is similar to that of full +-elimination in AADD: we show that we can iteratively eliminate the last variable in linear time. Let  $X = \{x_1, \dots, x_n\} \subseteq \mathcal{X}$  and  $y \in \mathcal{X}$ ; let  $\alpha$  be an SLDD $_×$  formula over  $X \cup \{y\}$ , ordered in such a way that  $x_1 \triangleleft \dots \triangleleft x_n \triangleleft y$ . To simplify the proof, we suppose that every variable in  $X \cup \{y\}$  is mentioned in every path of  $\alpha$ . Lemma A.24 shows that this can be done in polynomial time.

We have the following:

$$\begin{aligned} \text{Elim}_\times(\alpha) &= \prod_{\vec{z} \in D_X \times D_y} f_{\alpha}^{\text{SLDD}_\times}(\vec{z}) \\ &= \prod_{\vec{x} \in D_X} \prod_{\vec{y} \in D_y} f_{\alpha}^{\text{SLDD}_\times}(\vec{x} \cdot \vec{y}) \end{aligned}$$

For a given assignment  $\vec{x} \cdot \vec{y}$ , let us consider the path  $p$  in  $\alpha$  corresponding to  $\vec{x} \cdot \vec{y}$ . The path  $p$  contains  $n+1$  arcs: we denote as  $\Phi_{\vec{x}}$  the product of the  $\varphi$ -labels of the first  $n$  arcs and the offset, and as  $\varphi_{\vec{x} \cdot \vec{y}}$  the  $\varphi$ -label of the last arc. By definition of the interpretation function of SLDD $_×$ ,

$$f_{\alpha}^{\text{SLDD}_\times}(\vec{x} \cdot \vec{y}) = \Phi_{\vec{x}} \times \varphi_{\vec{x} \cdot \vec{y}},$$

and since  $\Phi_{\vec{x}}$  does not depend on  $\vec{y}$ , we get

$$\begin{aligned} \text{Elim}_\times(\alpha) &= \prod_{\vec{x} \in D_X} \prod_{\vec{y} \in D_y} \Phi_{\vec{x}} \times \varphi_{\vec{x} \cdot \vec{y}} \\ &= \prod_{\vec{x} \in D_X} \left( \Phi_{\vec{x}} \prod_{\vec{y} \in D_y} \varphi_{\vec{x} \cdot \vec{y}} \right). \end{aligned}$$

Hence, it is possible to transform  $\alpha$  into another SLDD $_×$  formula  $\alpha'$  that does not mention  $y$  and verifies  $\text{Elim}_\times(\alpha') = \text{Elim}_\times(\alpha)$  as follows. For each  $y$ -labeled

Table 3: Results about basic queries, optimization, and  $\gamma$ -cutting, together with the number of the proposition (or corollary) proving each claim. The symbol legend is in Table 1.

Query	ADD	SLDD <sub>+</sub>	SLDD <sub>×</sub>	AADD
<b>EQ</b>	√ A.9	√ A.9	√ A.9	√ A.9
<b>SE</b>	√ A.34	√ A.36	√ A.35	?
<b>OPT<sub>max</sub>, OPT<sub>min</sub></b>	√ A.13	√ A.13	√ A.13	√ A.13
<b>CT<sub>max</sub>, CT<sub>min</sub></b>	√ A.15	√ A.15	√ A.15	√ A.15
<b>ME<sub>max</sub>, ME<sub>min</sub></b>	√ A.15	√ A.15	√ A.15	√ A.15
<b>MX<sub>max</sub>, MX<sub>min</sub></b>	√ A.15	√ A.15	√ A.15	√ A.15
<b>CUT<sub>max</sub>, CUT<sub>min</sub></b>	√ A.14	√ A.14	√ A.14	√ A.14
<b>VA<sub>~γ</sub></b>	√ A.12	√ A.17	√ A.17	√ A.17
<b>VA<sub>≥γ</sub>, VA<sub>≤γ</sub></b>	√ A.12	√ A.16	√ A.16	√ A.16
<b>CO<sub>~γ</sub></b>	√ A.12	○ A.18	○ A.18	○ A.18
<b>CO<sub>≥γ</sub>, CO<sub>≤γ</sub></b>	√ A.12	√ A.16	√ A.16	√ A.16
<b>ME<sub>~γ</sub></b>	√ A.12	○ A.21	○ A.21	○ A.21
<b>ME<sub>≥γ</sub>, ME<sub>≤γ</sub></b>	√ A.12	√ A.23	√ A.23	√ A.23
<b>MX<sub>~γ</sub></b>	√ A.12	○ A.21	○ A.21	○ A.21
<b>MX<sub>≥γ</sub>, MX<sub>≤γ</sub></b>	√ A.12	√ A.22	√ A.22	√ A.22
<b>CUT<sub>~γ</sub></b>	√ A.11	● A.27	● A.27	● A.27
<b>CUT<sub>≥γ</sub>, CUT<sub>≤γ</sub></b>	√ A.11	● A.28	● A.29	● A.29
<b>CT<sub>~γ</sub></b>	√ A.12	○ A.21	○ A.21	○ A.21
<b>CT<sub>≥γ</sub>, CT<sub>≤γ</sub></b>	√ A.12	○ A.26	○ A.26	○ A.26

node  $N$ , we compute the value  $\varphi_N = \times_{a \in \text{Out}(N)} q_a$ , then we update the  $\varphi$ -label of each arc  $a_{\text{In}} \in \text{In}(N)$  so that it becomes  $\varphi(a_{\text{In}}) \times \varphi_N$ , and finally, we merge all the  $y$ -nodes into a new leaf. The process is linear in the size of  $\alpha$ , and it should be clear that

$$f_{\alpha'}^{\text{SLDD} \times}(\vec{x}) = \Phi_{\vec{x}} \prod_{\vec{y} \in D_y} \varphi_{\vec{x} \cdot \vec{y}},$$

and thus that

$$\text{Elim}_{\times}(\alpha) = \prod_{\vec{x} \in D_x} f_{\alpha'}^{\text{SLDD} \times}(\vec{x}) = \text{Elim}_{\times}(\alpha').$$

Consequently, it is possible to eliminate the last variable in an SLDD<sub>×</sub> formula in linear time, without changing the value of the “full” variable elimination. Computing this value can thus be done by eliminating every variable iteratively in a bottom-up way, stopping when the formula is reduced to a leaf and an offset; since the size of the formula is always decreasing, the overall procedure is polynomial-time.  $\square$

**Corollary A.45.** *ADD and SLDD<sub>×</sub> satisfy  $\times$ Marg.*

*Proof.* Full variable  $\times$ -elimination is polynomial on SLDD<sub>×</sub>. Since any ADD formula can be turned into an equivalent SLDD<sub>×</sub> formula in linear time (Proposition A.7), full variable  $\times$ -elimination is also polynomial on ADD. Hence, from Lemma A.40, we get that ADD and SLDD<sub>×</sub> satisfy  $\times$ Marg.  $\square$

Table 4: Results about transformations, together with the number of the proposition (or corollary) proving each claim. The symbol legend is in Table 1.

Transformation	ADD	SLDD <sub>+</sub>	SLDD <sub>×</sub>	AADD
<b>CD</b>	√ A.10	√ A.10	√ A.10	√ A.10
<b>maxBC, minBC</b>	√ A.33	● A.31	● A.31	● A.31
<b>+BC</b>	√ A.33	√ A.33	● A.32	● A.32
<b>×BC</b>	√ A.33	● A.32	√ A.33	● A.32
<b>maxC, minC</b>	● A.30	● A.30	● A.30	● A.30
<b>+C</b>	● A.30	● A.30	● A.30	● A.30
<b>×C</b>	● A.30	● A.30	● A.30	● A.30
<b>maxElim, minElim</b>	● A.38	● A.38	● A.38	● A.38
<b>+Elim</b>	● A.38	● A.38	● A.38	● A.38
<b>×Elim</b>	● A.38	● A.38	● A.38	● A.38
<b>SmaxElim, SminElim</b>	● A.38	● A.38	● A.38	● A.38
<b>S+Elim</b>	● A.38	● A.38	● A.38	● A.38
<b>S×Elim</b>	● A.38	● A.38	● A.38	● A.38
<b>SBmaxElim, SBminElim</b>	√ A.39	● A.39	● A.39	● A.39
<b>SB+Elim</b>	√ A.39	√ A.39	● A.39	● A.39
<b>SB×Elim</b>	√ A.39	● A.39	√ A.39	● A.39
<b>maxMarg, minMarg</b>	√ A.41	√ A.41	√ A.41	√ A.41
<b>+Marg</b>	√ A.43	√ A.43	√ A.43	√ A.43
<b>×Marg</b>	√ A.45	?	√ A.45	?